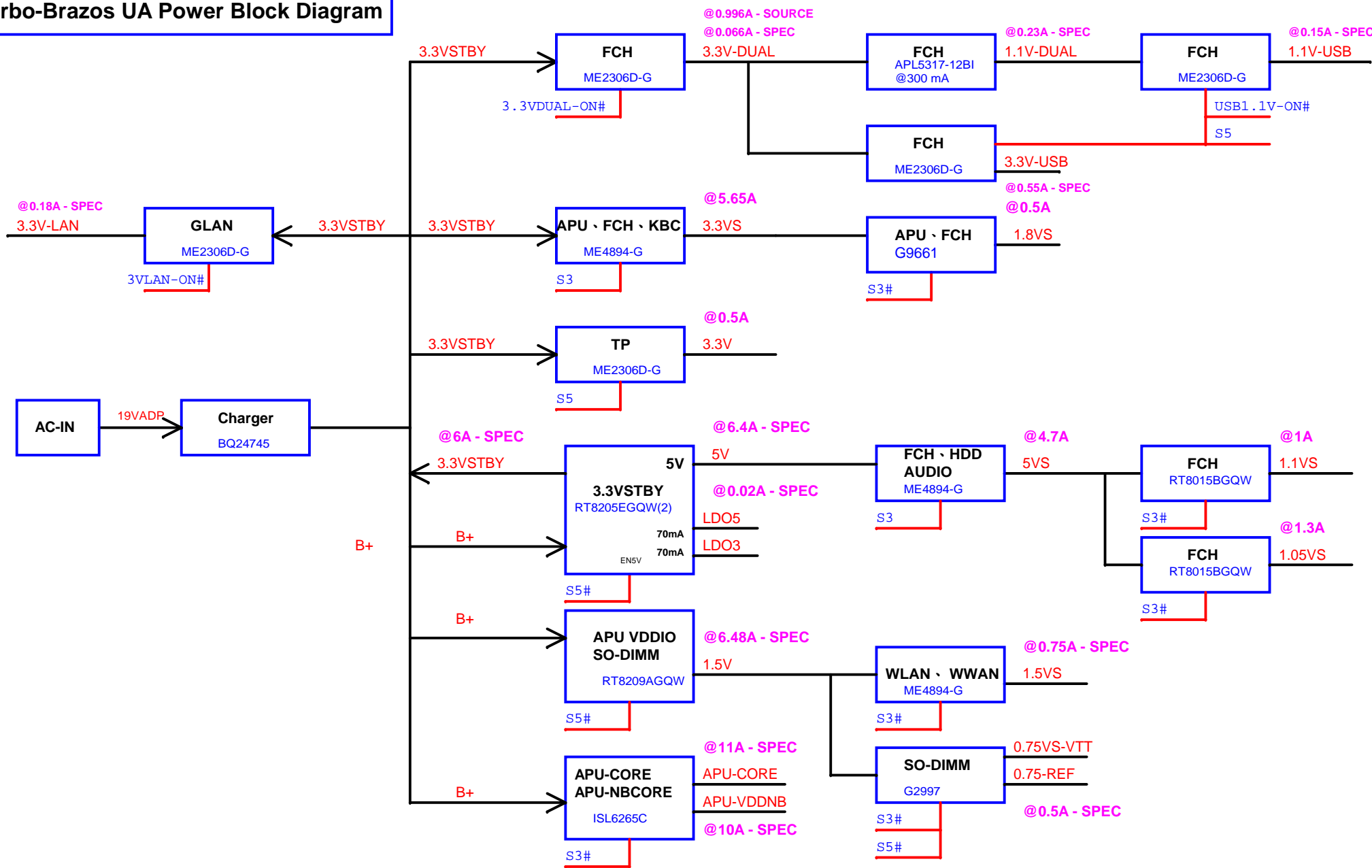


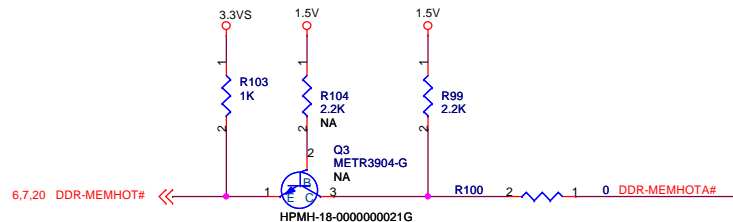
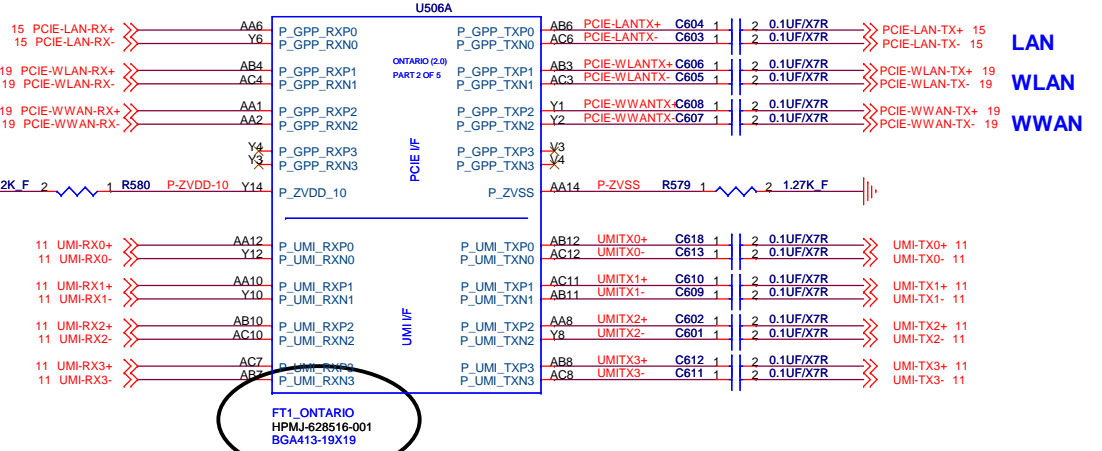
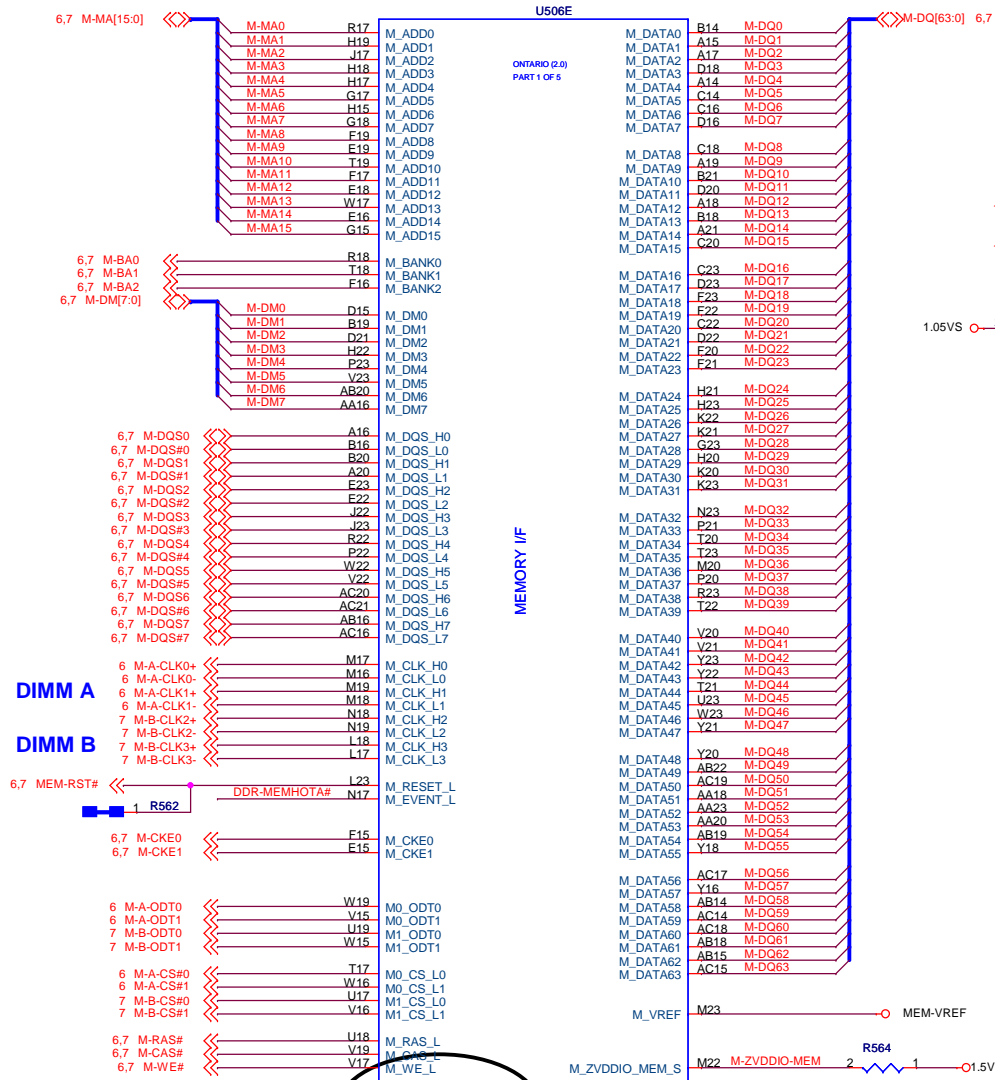
## 1



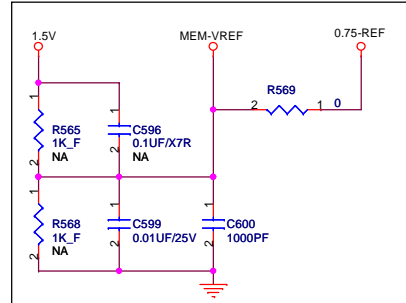
# Garbo-Brazos UA Power Block Diagram



# APU\_MEMORY/PCIE/UMI



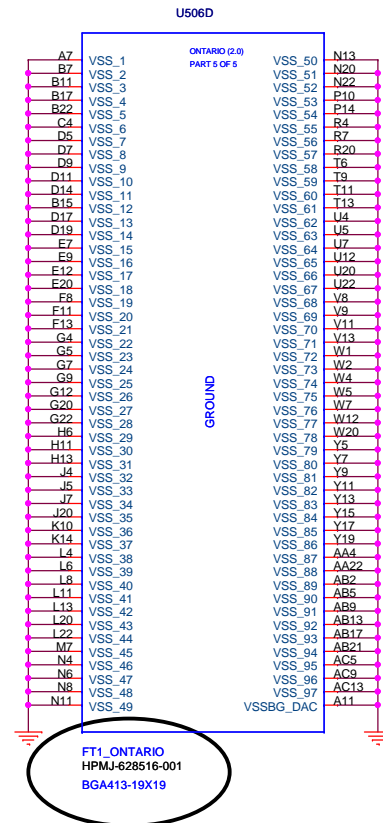
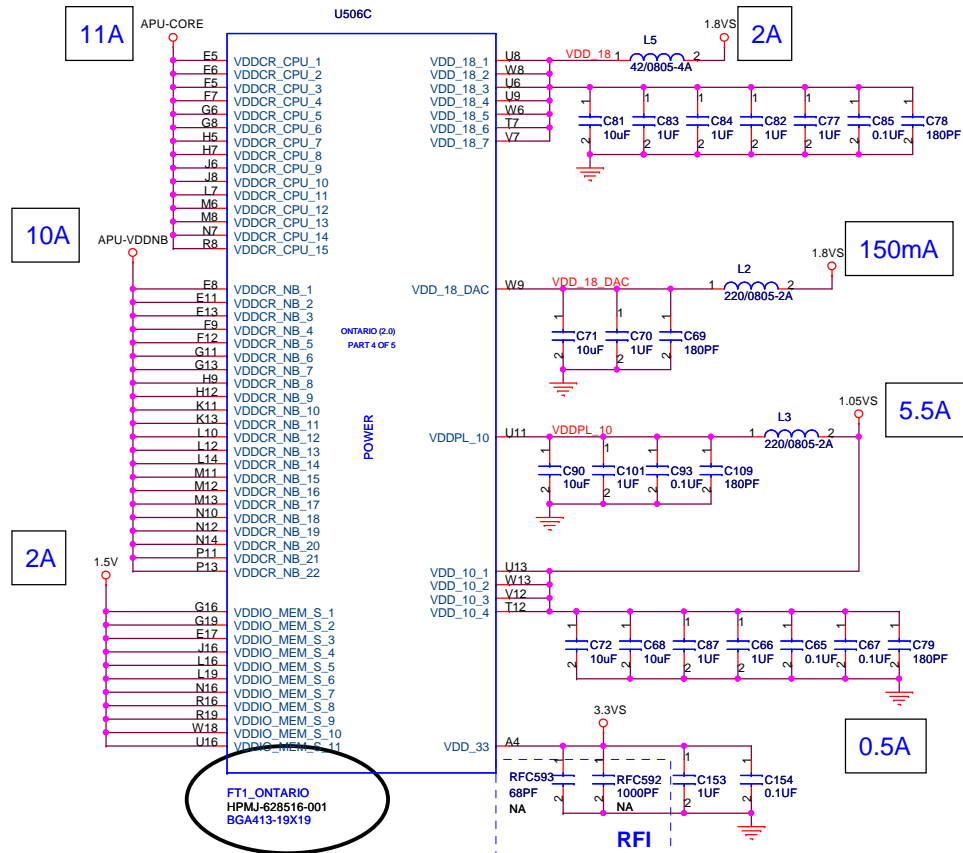
Place close APU pin M23



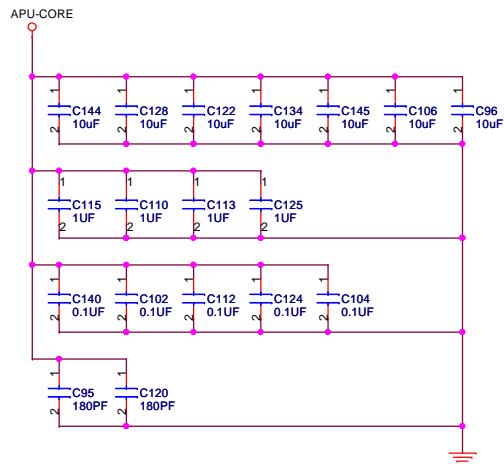
**FLEX Computing**



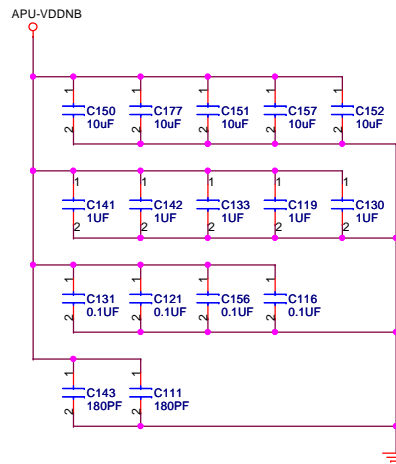
# APU\_POWER & GND



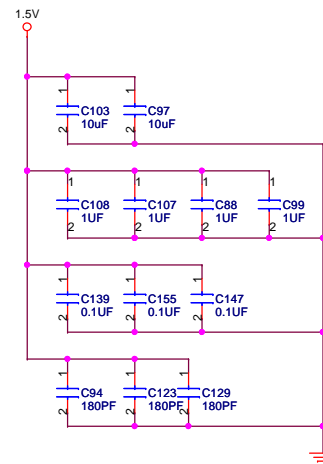
## For APU-CORE



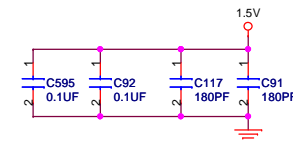
## For APU-VDDNB



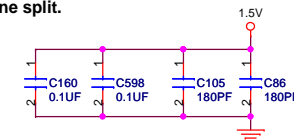
## For 1.5V



If the VSS plane is cut to create a VDDIO\_MEM\_S plane, ceramic capacitors are connected across the VDDIO\_MEM\_S and VSS plane split.



If the VSS plane is longer than 63.5 mm an additional two capacitors are required across the VDDIO\_MEM\_S and VSS plane split.



**FLEX Computing**

Project Name : H210UA1		Title : APU_POWER / GND	
Size :	Document Number :	HPMH-40GAB6000-D000	Rev : D
Date : Friday, November 19, 2010		Sheet : 5	of 28

1



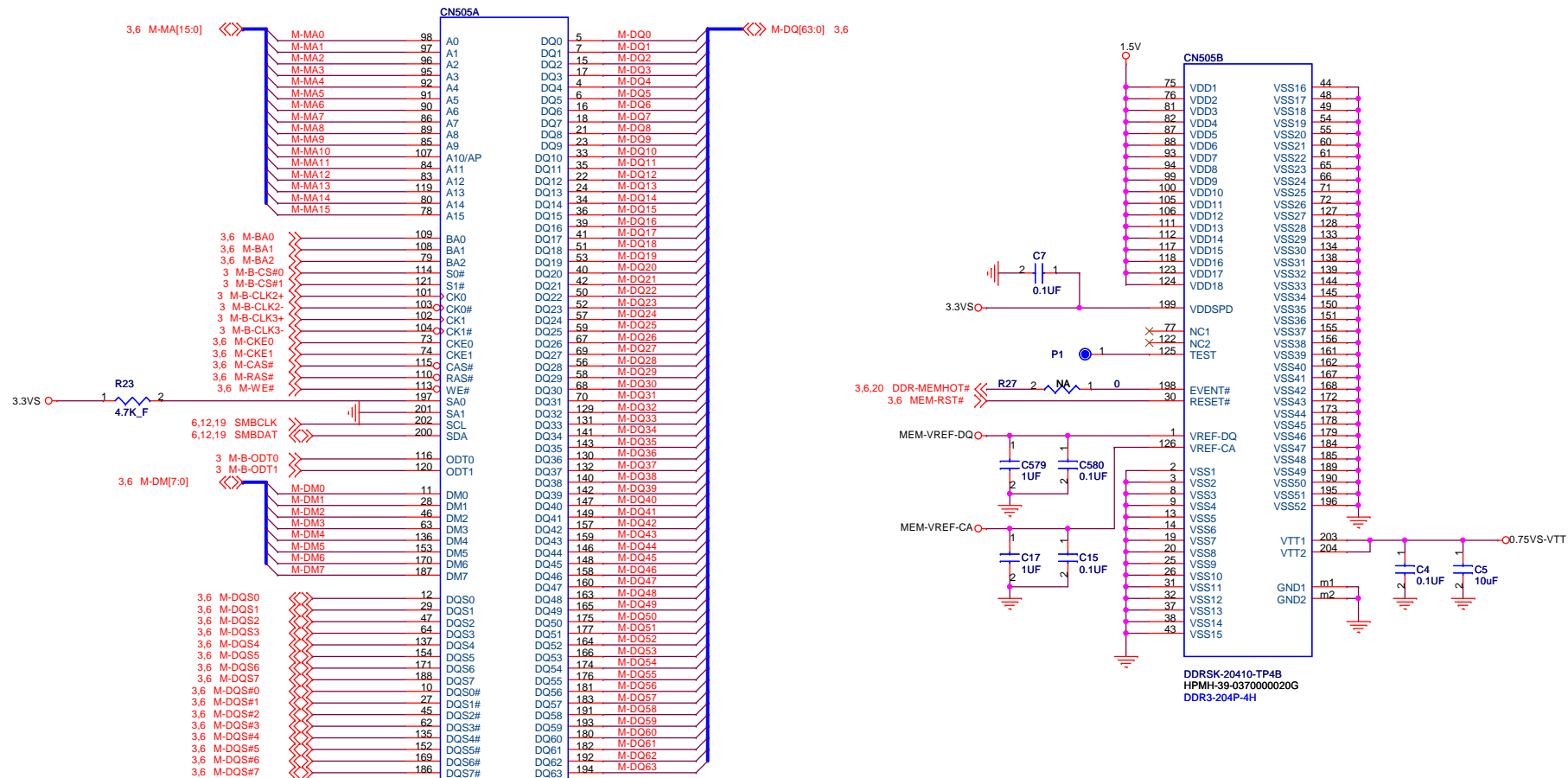
2

L

1

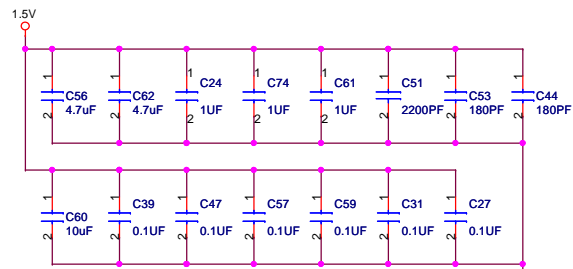


# DDR3 DIMM B

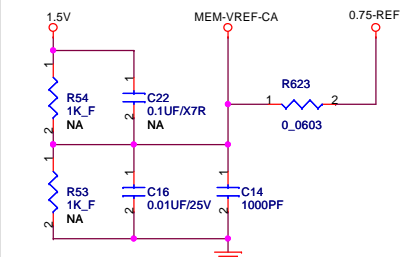


SA0	SA1	Address
0	0	A0
1	0	A2

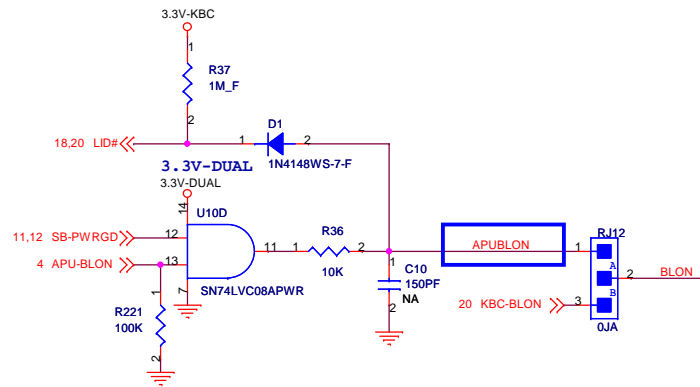
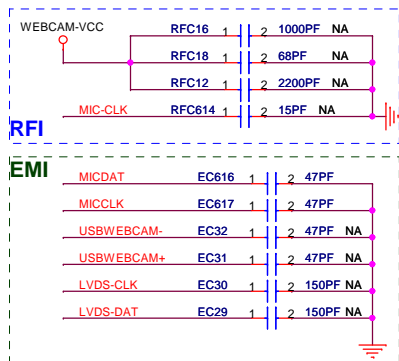
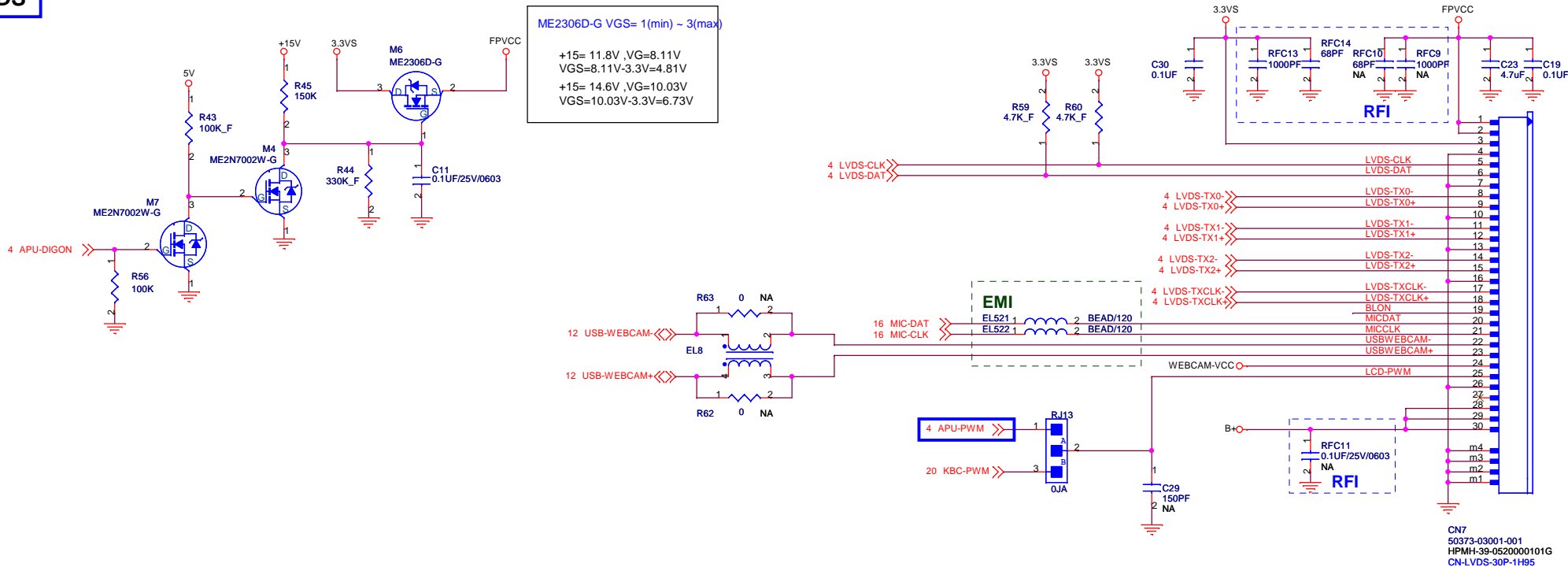
Layout :  
Place these Caps near So-DimmB



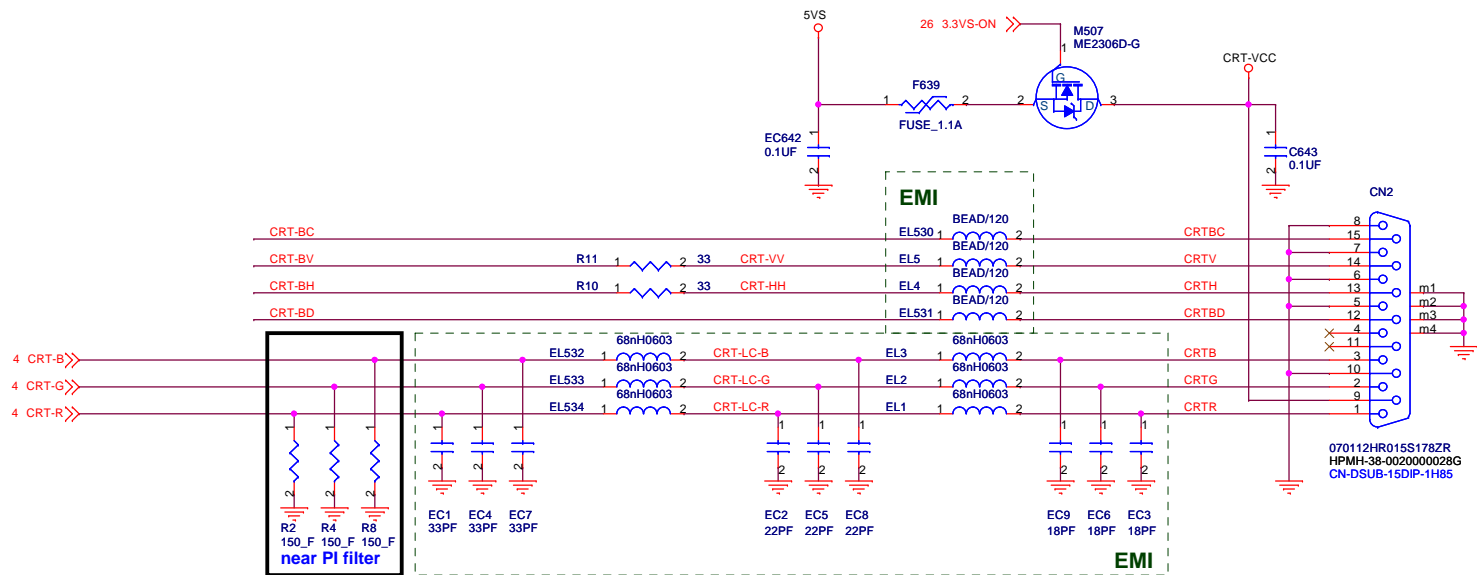
Layout :  
Place near So-DimmB



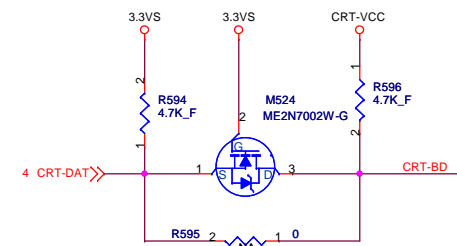
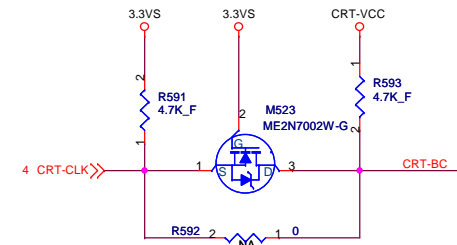
# LVDS



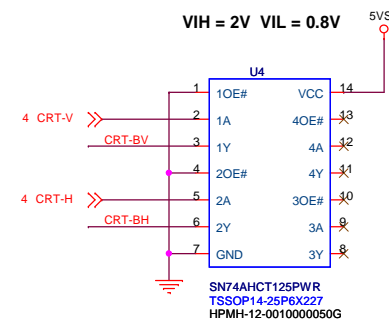
# CRT



# CRT LEVEL SHIFT



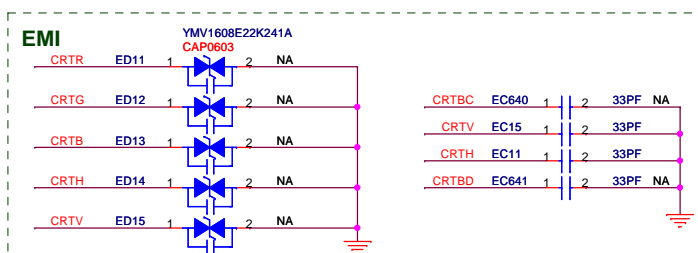
# H / V SYNC BUFFER



INPUT		OUTPUT
nOE	nA	nY
L	L	L
L	H	H
H	X	Z

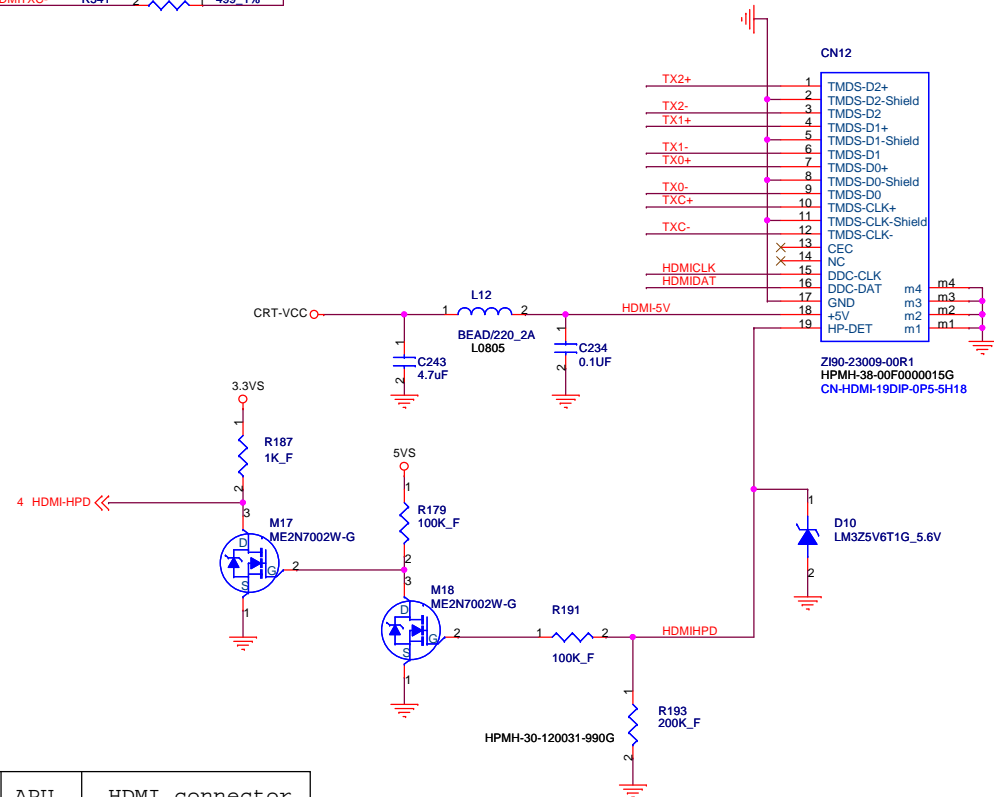
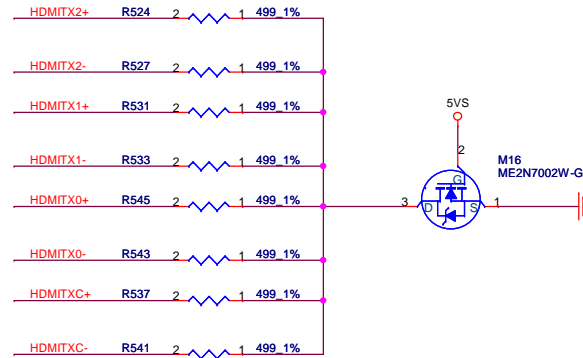
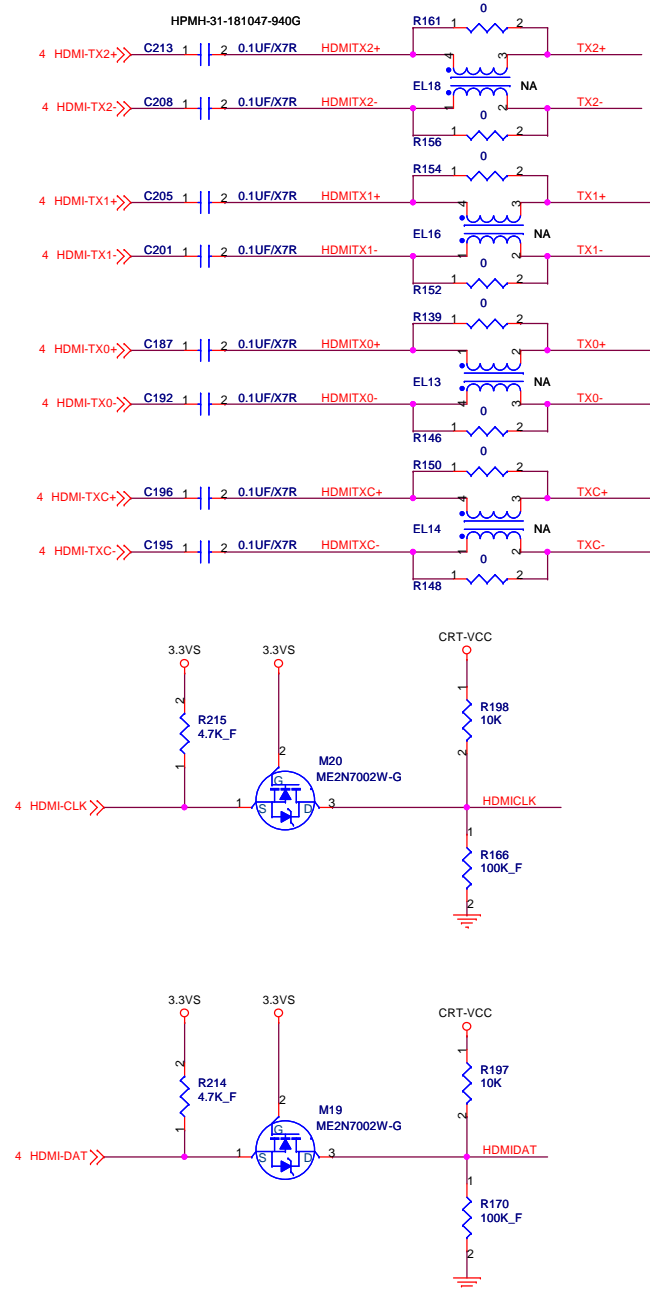
**FLEX** Computing

Project Name : H210UA1		Title : CRT Connector	
Size : Custom	Document Number : HPMH-40GAB6000-D000	Rev : D	
Date : Monday, November 22, 2010	Sheet : 9	of	28



## CLOSE CN514

Connected a 499-Ω 5% resistor on each signal connected with a FET to GND (one FET per pair) located on the TMDSS connector side of the series capacitors.



PLUG	IN/OUT	APU	HDMI connector
	IN	H	H
	OUT	L	L

## 1



## 2

1

100



100



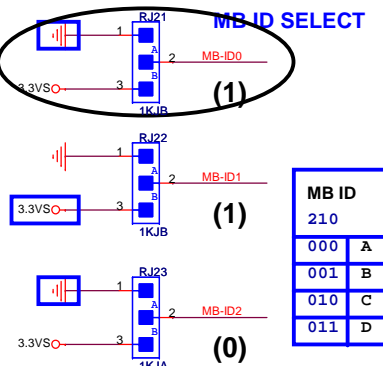
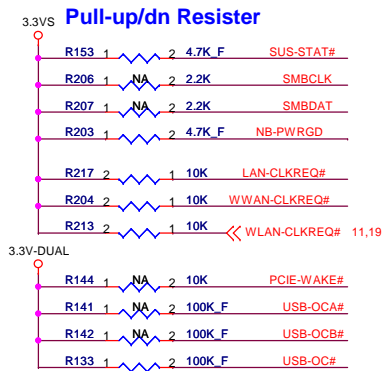
1000



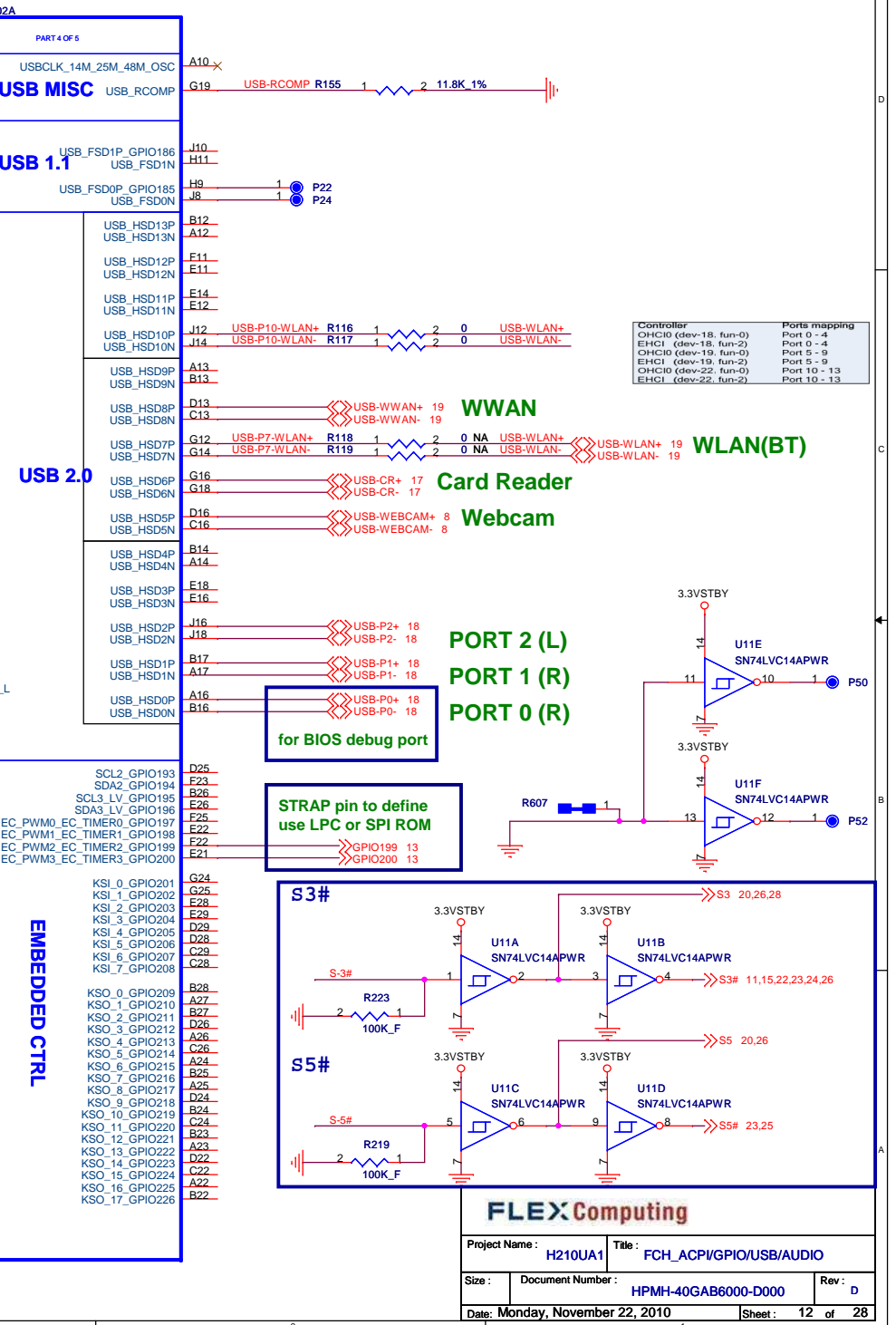
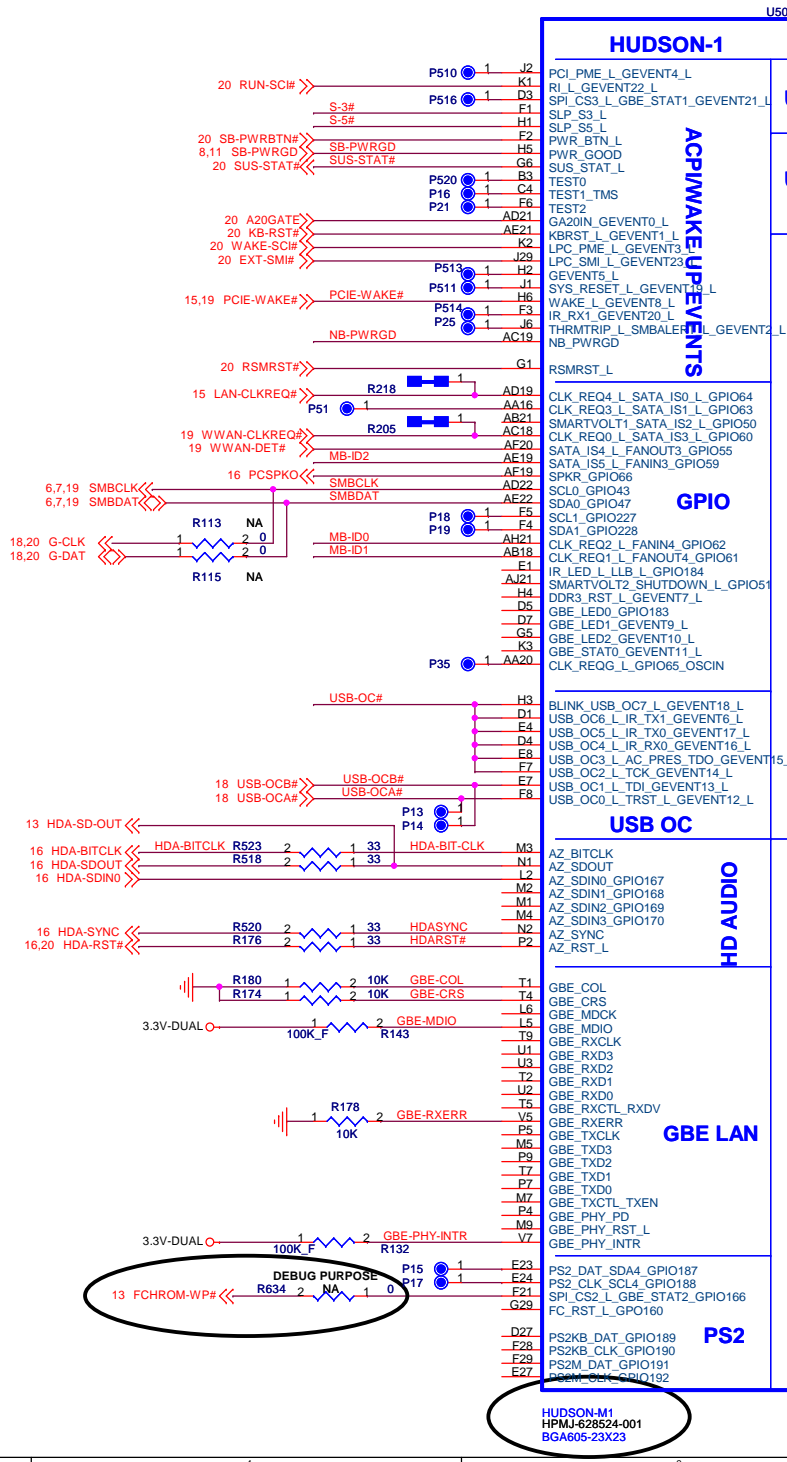
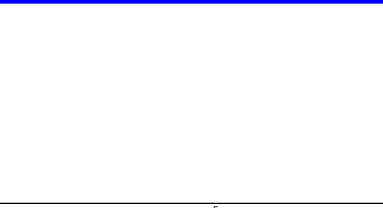
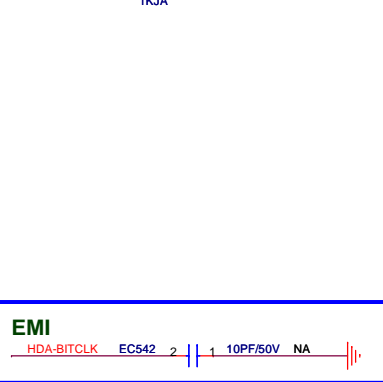
\_\_\_\_\_



## FCH\_ACPI/GPIO/USB/AUDIO



MB ID	
210	
000	A
001	B
010	C
011	D



2



## 1

## 1

**FCH\_PWR/GND**

**HUDSON-1 (PART 3 OF 5)**

**HUDSON-1 (PART 5 OF 5)**

**U502C**

**U502D**

**3.3V-USB / 1.1V-USB**

**AC mode**

**DC mode**

**FCH\_PWR/GND**

**Project Name: H210UA1**

**Title: FCH\_PWR/GND**

**Size: Document Number: HPMH-40GAB6000-D000**

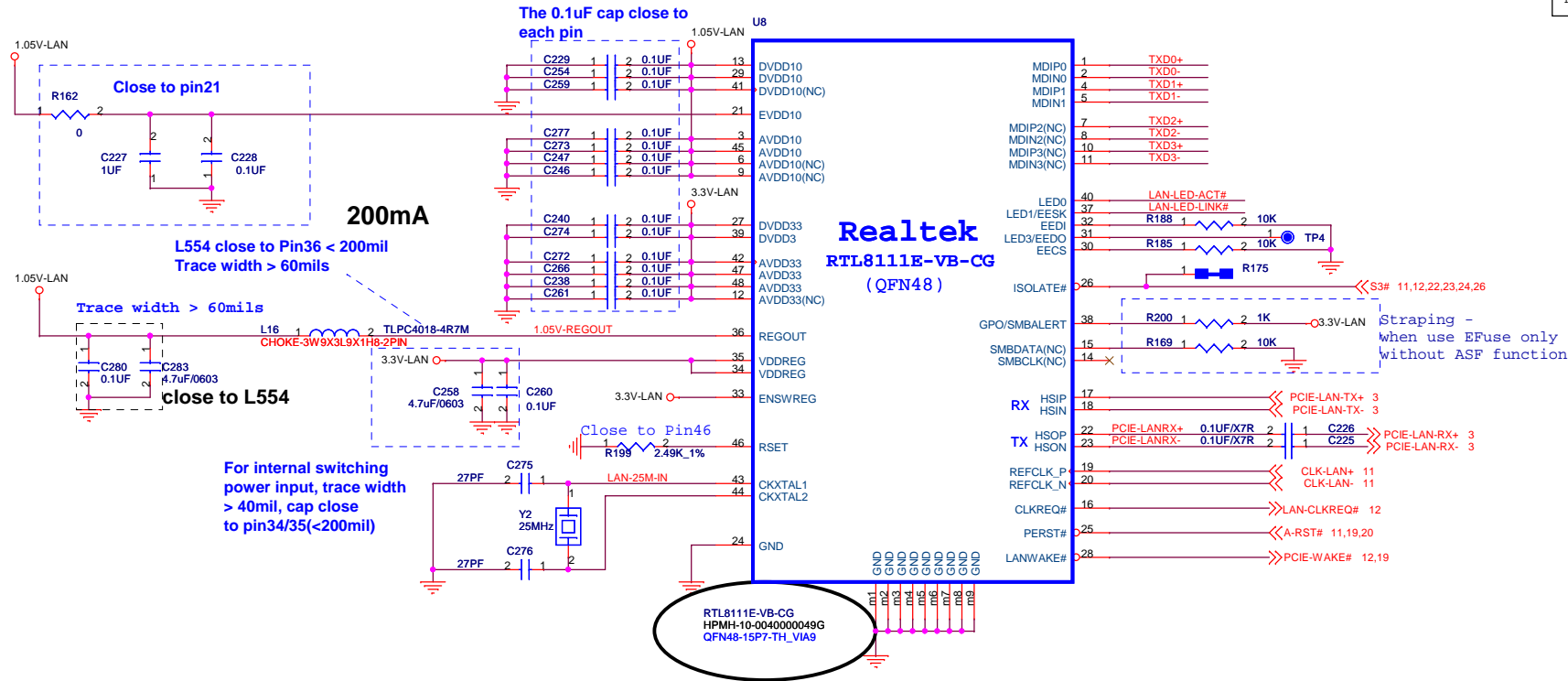
**Rev: D**

**Date: Friday, November 19, 2010**

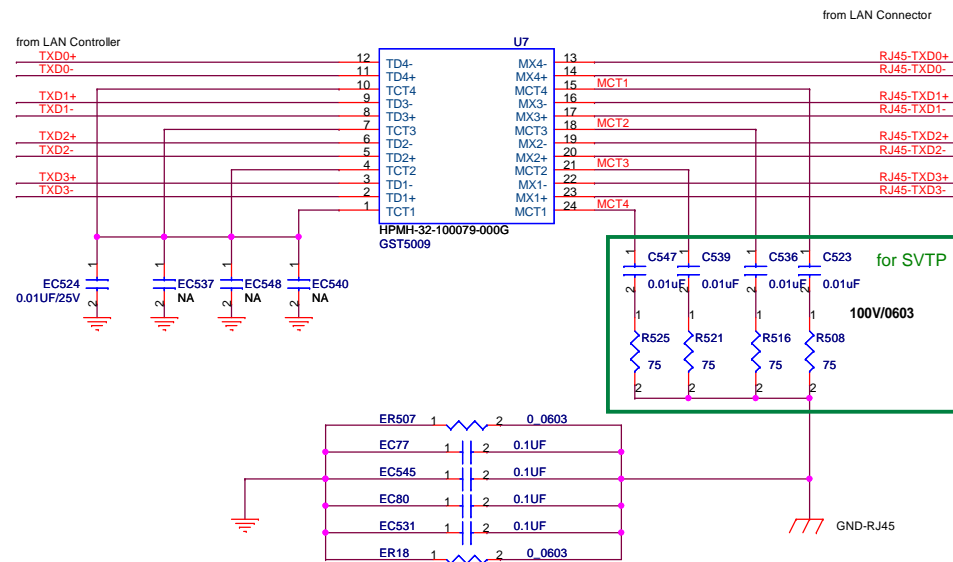
**Sheet: 14 of 28**

## GLAN Controller

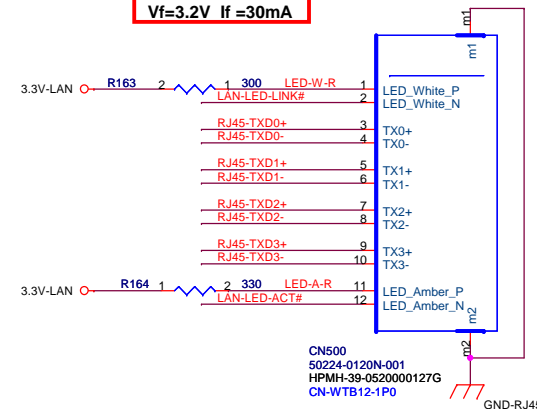
P/N	IEEE support	Process
RTL8111E-VS-CG	Non-IEEE	Copper
RTL8111E-VB-CG	IEEE	Copper
RTL8111E-VB-GR	IEEE	Gold



## RJ45 connector

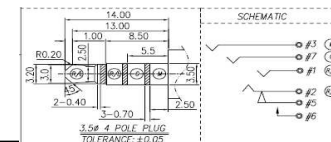
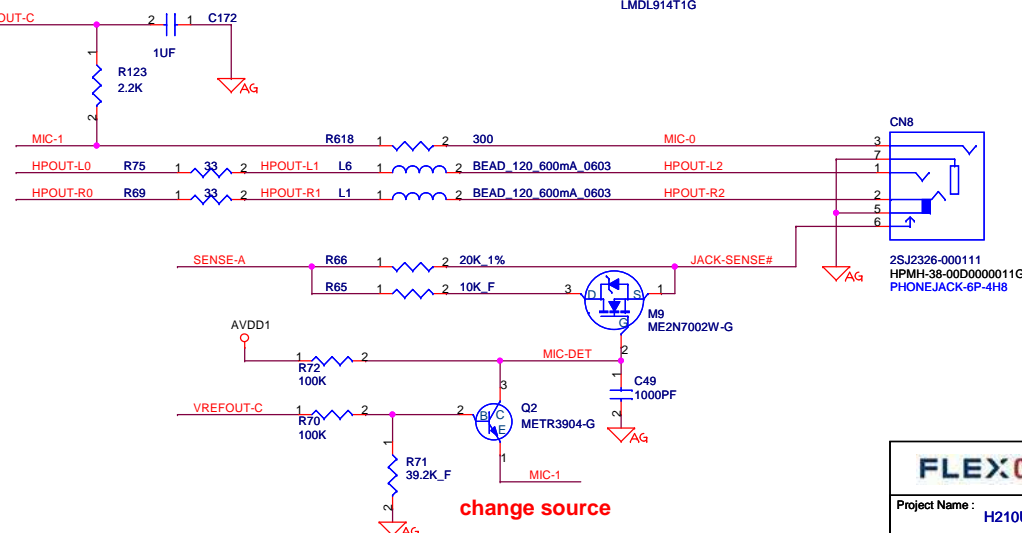
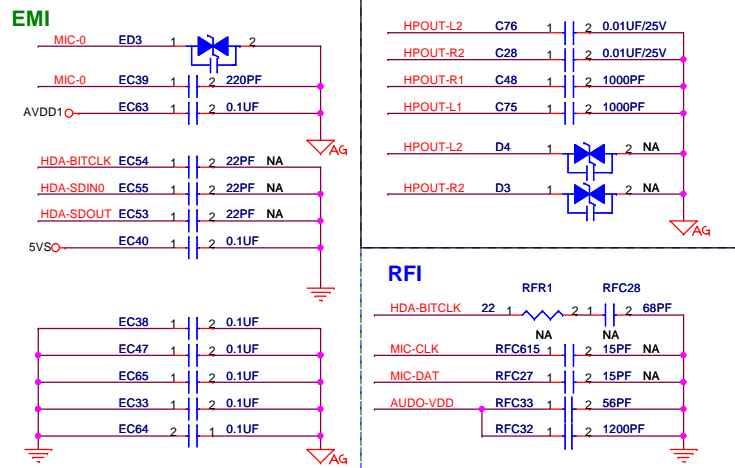
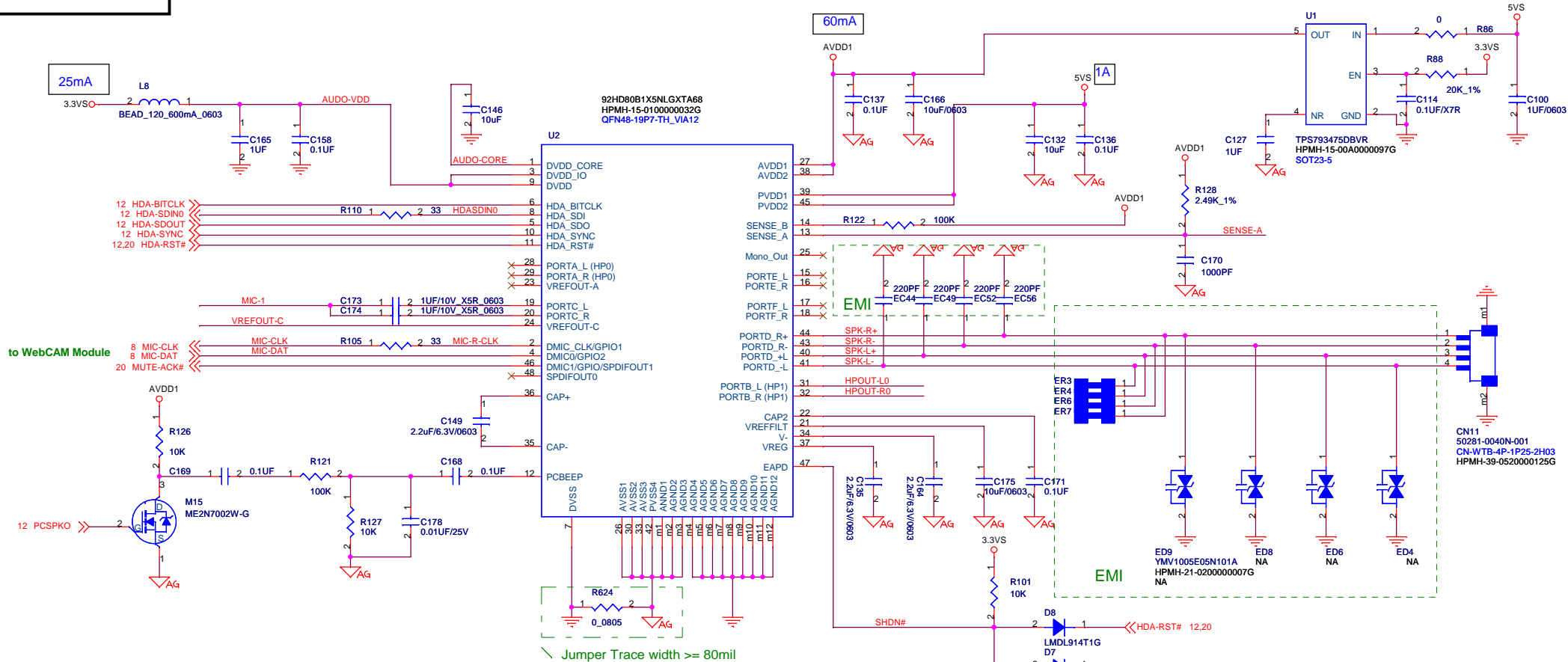


Amber  
Vf=2V If =20mA  
White  
Vf=3.2V If =30mA

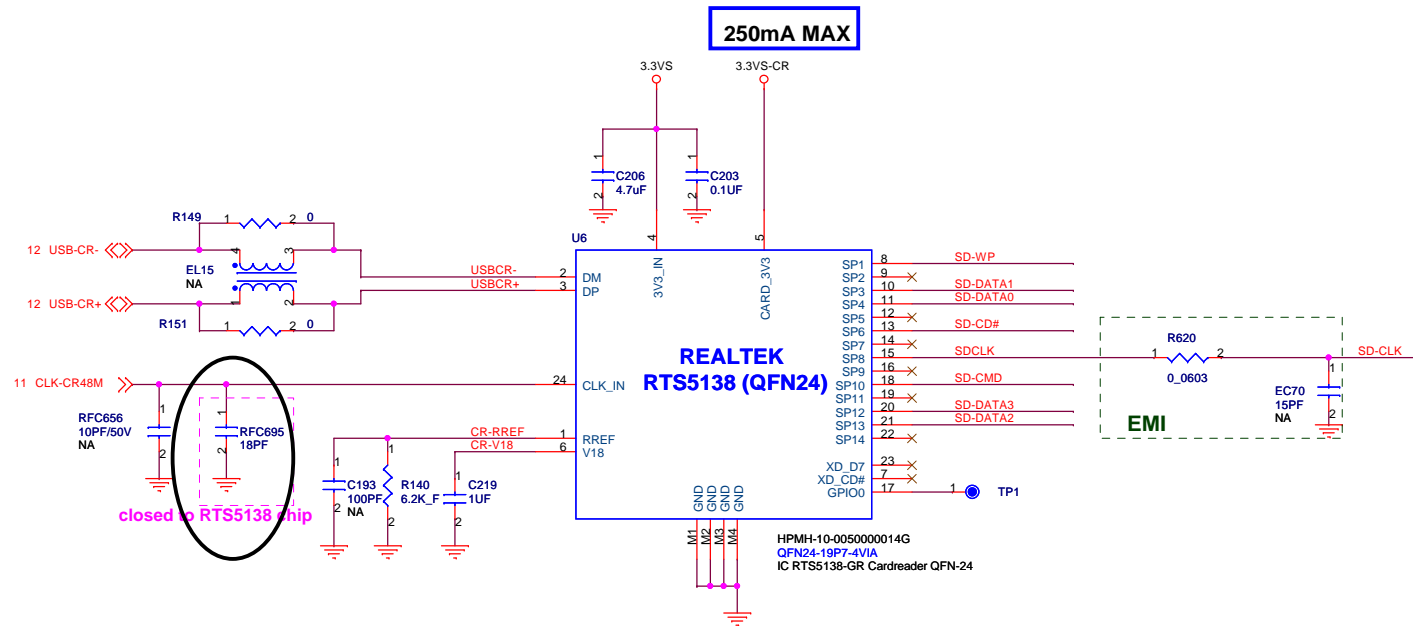


<b>FLEX</b> Computing			
Project Name : <b>H210UA1</b>		Title : <b>GLAN - RTL8111E-VS-CG</b>	
Size :	Document Number : <b>HPMH-40GAB6000-D000</b>		Rev : <b>D</b>
Date: <b>Monday, November 22, 2010</b>		Sheet : <b>15</b> of <b>28</b>	

## Audio CODEC

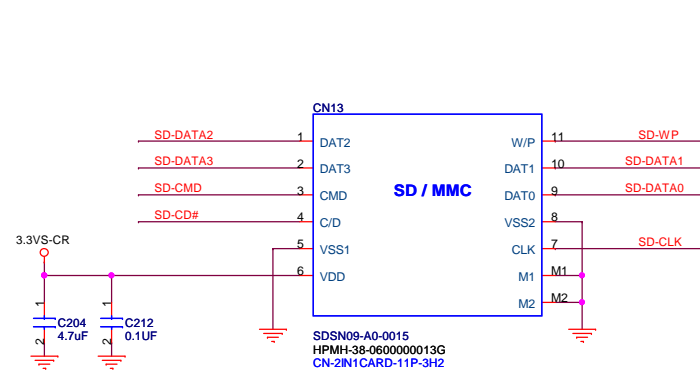


## Card Reader



SD/MMC	
SP1	SD_WP
SP2	
SP3	SD_D1
SP4	SD_D0
SP5	SD_D7
SP6	SD_CD#
SP7	SD_D6
SP8	SD_CLK
SP9	SD_D5
SP10	SD_CMD
SP11	SD_D4
SP12	SD_D3
SP13	SD_D2
SP14	

## Memory Card Socket



SD/MMC	
1	DAT2
2	DAT3
3	CMD
4	C/D
5	VSS1
6	VDD
7	CLK
8	VSS2
9	DAT0
10	DAT1
11	W/P

**FLEX Computing**

Project Name :	H210UA1	Title :	RTS5138 CardReader
Size :	Custom	Document Number :	HPMH-40GAB6000-D000
Date :	Monday, November 22, 2010	Rev :	D
Sheet :	17	of	28

**USB Port 0 (R)**

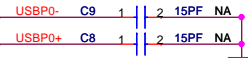
The schematic diagram illustrates the electrical connections for USB Port 0 (R). Key components and connections include:

- USB Connectors:** 12 USB-P0 and 12 USB-P0+ are connected to the circuit.
- Resistors:** R32 (0 NA) and R31 (0 NA) are used for signal conditioning.
- Inductor:** EL6 is connected in series with the USB-P0+ line.
- ESD Protection:** ESD1 (ESD\_PACDN004SR) is connected to the USB-P0 line.
- Diode:** A Schottky diode is connected between USB5VA and the USB-P0 line.
- Capacitors:** C924 (100UF/POS6.3V) and C622 (0.1UF) are used for decoupling.
- ICs:** POS3528H14 (HPMH-31-0700000051G) and CN-USB-4DIP-2H3 (HPMH-38-0040000055G) are integrated into the circuit.

**Note 1**

For USB ports that have trace length of  $\leq 10"$ , the rise and fall time parameters may not meet the specification of  $> 450$  ps.

For USB ports that have trace length of  $\leq 10"$ , the rise and fall time parameters may not meet the specification of  $> 450$  ps.



# USB Port 1 (R)

The schematic diagram illustrates the electrical connections for USB Port 1 (R). Key components and connections include:

- USB Connector:** Labeled "12 USB-P1-" and "12 USB-P1+", connected to the USBP1- and USBP1+ signals.
- Resistors:** R52 and R51 (0 NA) are connected to the USBP1- and USBP1+ signals.
- Capacitors:** C923 (100UF/POS6.3V) and C621 (0.1UF) are connected to the USBP1- and USBP1+ signals.
- ESD Protection:** ESD\_PACDN004SR is connected to the USBP1- and USBP1+ signals.
- USB Controller:** POS3528H14 (HPMH-31-0700000051G) is connected to the USBP1- and USBP1+ signals.
- USB Hub:** CN6 (CN-USB-4DIP-2H3, HPMH-38-0040000055G) is connected to the USBP1- and USBP1+ signals.
- Power:** USB5VA is connected to the USBP1- and USBP1+ signals.

**Note 1**

USBP1- C13 1 2 15PF NA

USBP1+ C12 1 2 15PF NA



# USB Port 2 (L)

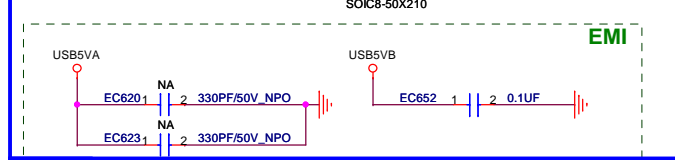
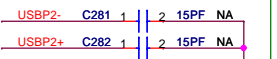
The schematic diagram illustrates the electrical connections for USB Port 2 (L). It shows the interface between the USB-P2 connector and the USB connector (CN14). The circuit includes a transformer (EL23) with resistors R210 and R216, a USB5VB regulator (ED10), and a USB connector (CN14). The connector is connected to a USB cable (CN-USB-4DIP-5H26). The diagram also shows the connection to the USB5VB regulator and the USB connector (CN14).

**Note 1**

For USB ports that have trace length of  $\leq 10"$ , the rise and fall time parameters may not meet the specification of  $> 450$  ps.

The diagram shows the connection of USB2- and USB2+ signals to a USB connector. The signals are connected to pins 1 and 2 of the connector, which are also connected to a 15PF capacitor and ground.

For USB ports that have trace length of  $\leq 10''$ , rise and fall time parameters may not meet the specification of  $> 450$  ps.



## LED

**Amber**  
 $V_f = 1.6V$   $I_f = 25mA$   
**White**  
 $V_f = 3.3V$   $I_f = 25mA$

**HDD LED** 5VS

D5  
A 3 R76 2 1 680  
K 2 R78 1 2 390

**Power LED(MB)** 5V

D6  
A 1 R93 2 1 680

**Debug**

NA  
D11  
A 1 R220 2 330  
K

**Amber**  
 $V_f = 1.6V$   $I_f = 25mA$   
**White**  
 $V_f = 3.3V$   $I_f = 25mA$

$V_f = 2.6V$   
 $I_f = 25mA$

$V_f = 2.2V$   
 $I_f = 25mA$

HPMH-37-0010000060G  
LED-126X040-3P

HPMH-37-0010000076G  
LED-2P-071X040

HPMH-37-0010000075G  
LED-2P-1P6X0P8

$V_f = 2.6V$   
 $I_f = 25mA$



# Webcam

From KBC

ME2N7002W-G NA

M10

ME2306D-G NA

M11

R68 100K\_F NA

R67 330K\_F NA

C33 0.01UF/25V NA

C40 0.01UF/25V NA

R625 0\_0603

15V

5V

WEBCAM-VIDEO

20 WEBCAM-ON#

ME2306D-G VGS= 1(min) ~ 3(max)

+15= 11.8V ,VG=8.65V  
VGS=8.65V-5V=3.65V

+15= 14.6V ,VG=10.7V  
VGS=10.7V-5V=5.7V

**WEBCAM MODULE PIN DEFINE**

3. Connector 16 pins , pitch 0.8

Pin 1 : ME2306D-G

Pin 2 : ME2306D-G

Pin 3 : GND

Pin 4 : GND

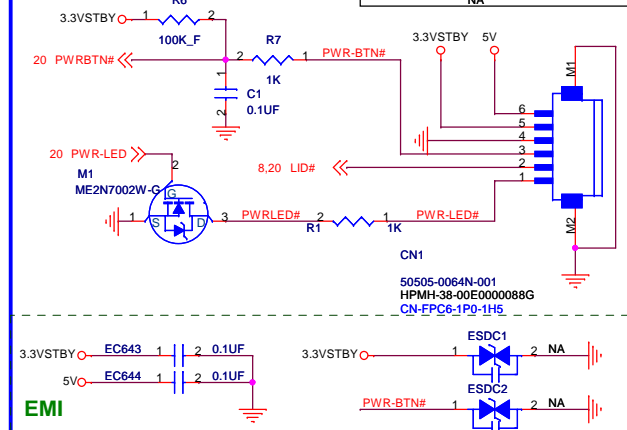
Pin 5 : DA

Pin 6 : VCC 5V

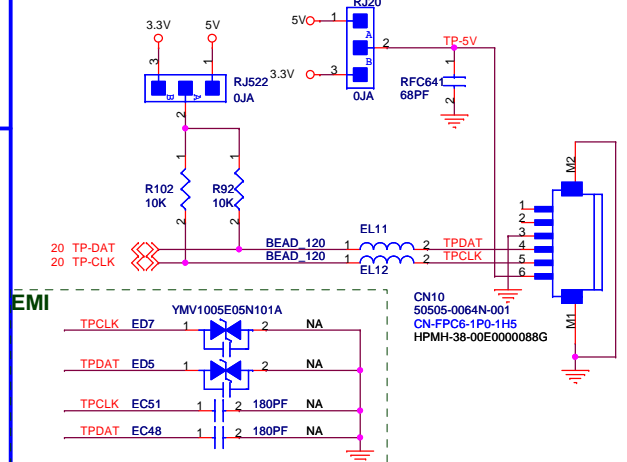
 $+15 = 14.6V, V_G = 10.7V$ 

Pin 1 : MIC\_CLK  
Pin 2 : MIC\_DATA  
Pin 3 : GND  
Pin 4 : D+  
Pin 5 : D-  
Pin 6 : VCC 5V

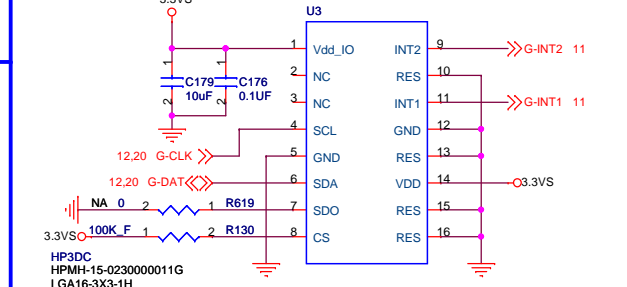
0  
R629 1 2 PWR-BTN#  
NA



# Touch Pad



U3



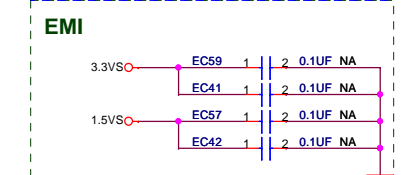
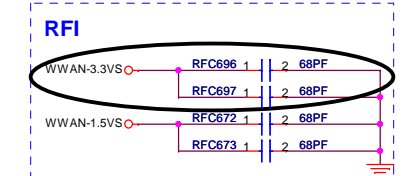
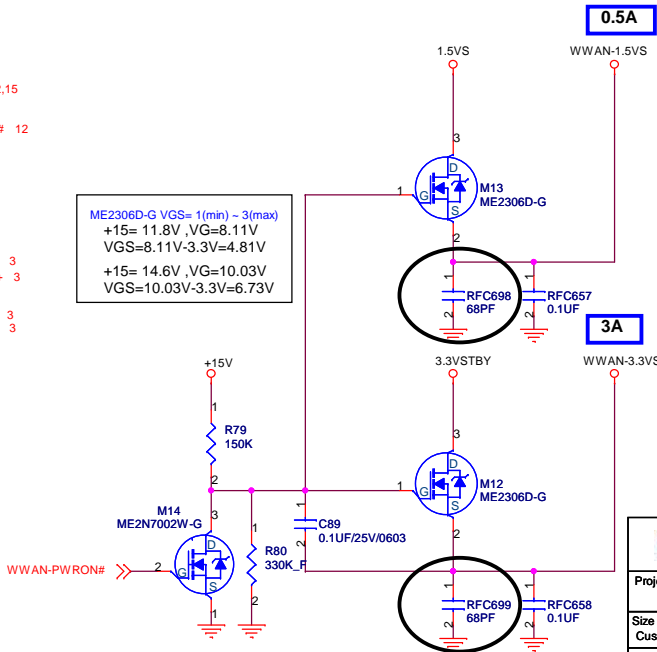
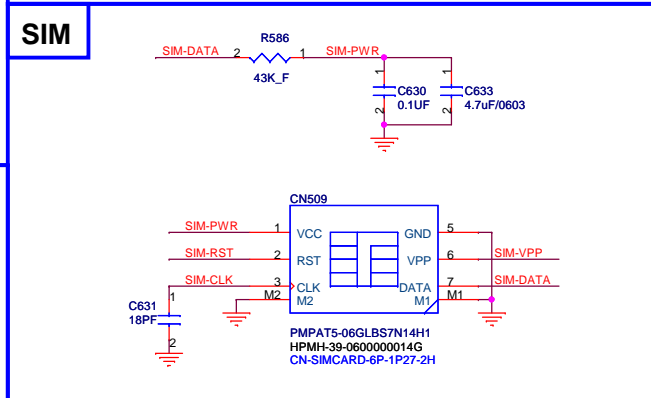
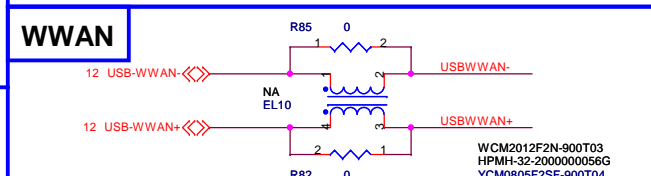
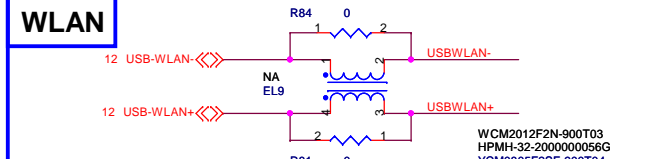
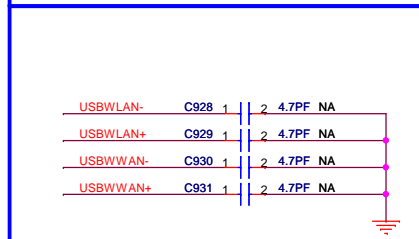
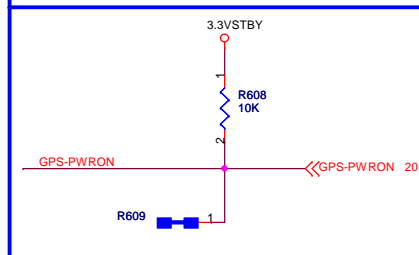
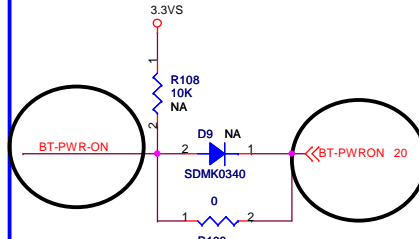
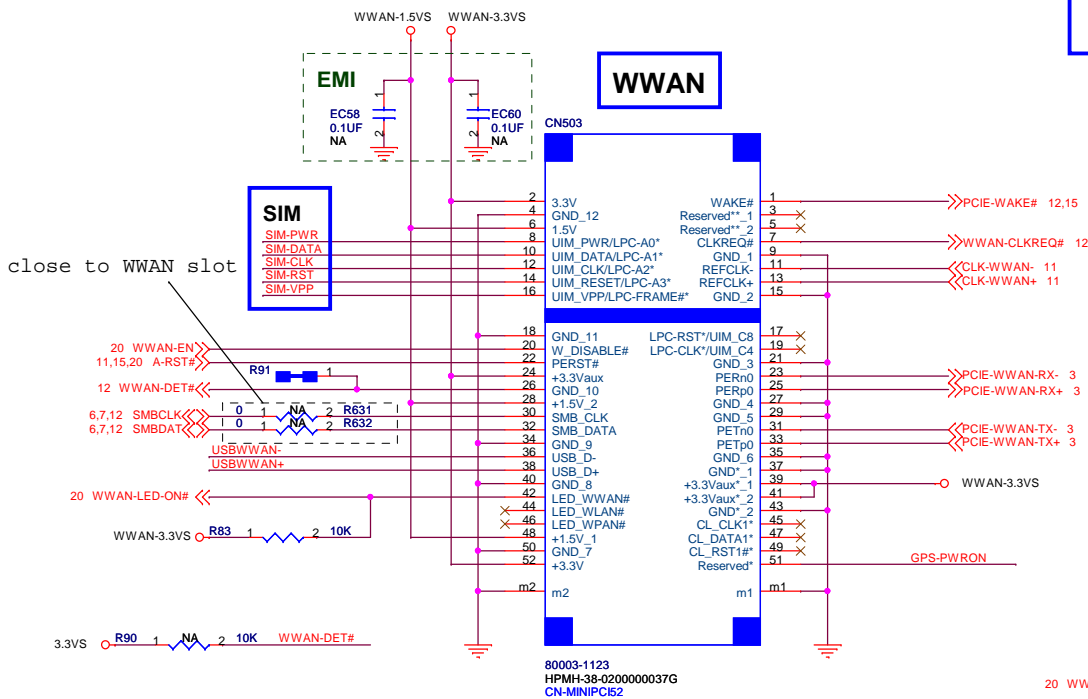
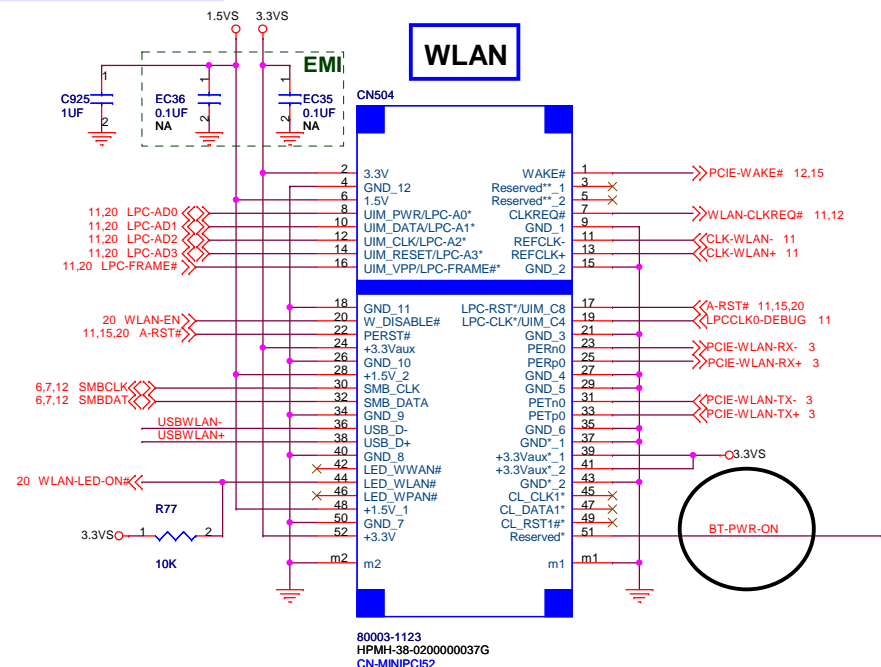
HP3DC Address:  
0011 0010 : 0x32

Custom	
M	M

Sheet : 10 of 20

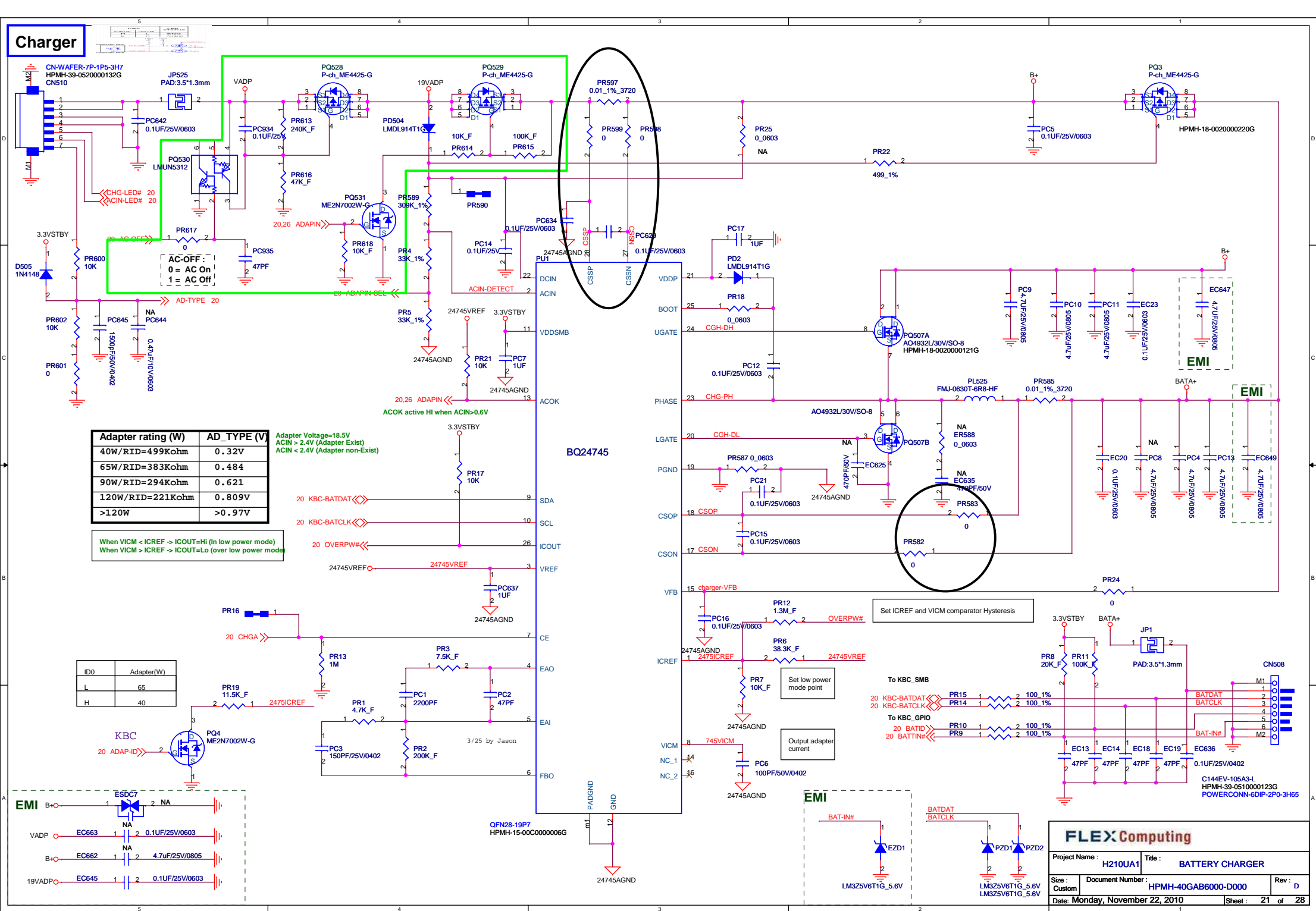
HPMH-40GAB6000-D000	D
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# WLAN / WWAN





Charger



Adapter rating (W)	AD_TYPE (V)
40W/RID=499Kohm	0.32V
65W/RID=383Kohm	0.484
90W/RID=294Kohm	0.621
120W/RID=221Kohm	0.809V
>120W	>0.97V

When VICM < ICREF -> ICOUT=Hi (in low power mode)  
When VICM > ICREF -> ICOUT=Lo (over low power mode)

ID0	Adapter(W)
L	65
H	40

**FLEX**Computing

Project Name : H210UA1Title : BATTERY CHARGER

Size : CustomDocument Number : HPMH-40GAB6000-D000Rev : D

Date : Monday, November 22, 2010Sheet : 21 of 28



## 1.5VDDR

EN_PSV	Voltage
Low	0.8V (Max)
High	2.9V (Min)
float	2V (Typ)

$$I_{OCP} = ((PR678 \cdot 10) / R_{ds(on)}) + I_{O(max)} / 3$$

$$= ((15 \cdot 10) / 15) + 3.7 = 13.7A$$

HPMH-15-00B0000134G  
QFN16-19P7-TH

**RT8209LGQW**

$$V_O = 0.75 \cdot (1 + (PR676 / PR677))$$

$$= 0.75 \cdot (1 + 1) = 1.5V$$

RFI

for noise issue, close to 1.5V

**Max=7.5A  
OCP=11A**

## 0.75VS

Table 1. S3 and S5 Control Table

STATE	S3	S5	VTT	VREF
Normal	Hi	Hi	1.25V/0.9V	1.25V/0.9V
Standby	Lo	Hi	12mV/6mV (High-Z)	1.25V/0.9V
Shutdown	Lo	Lo	0V (Discharge)	0V (Discharge)
Shutdown	Hi	Lo	0V (Discharge)	0V (Discharge)

**Max=0.5A  
OCP=0.75A**

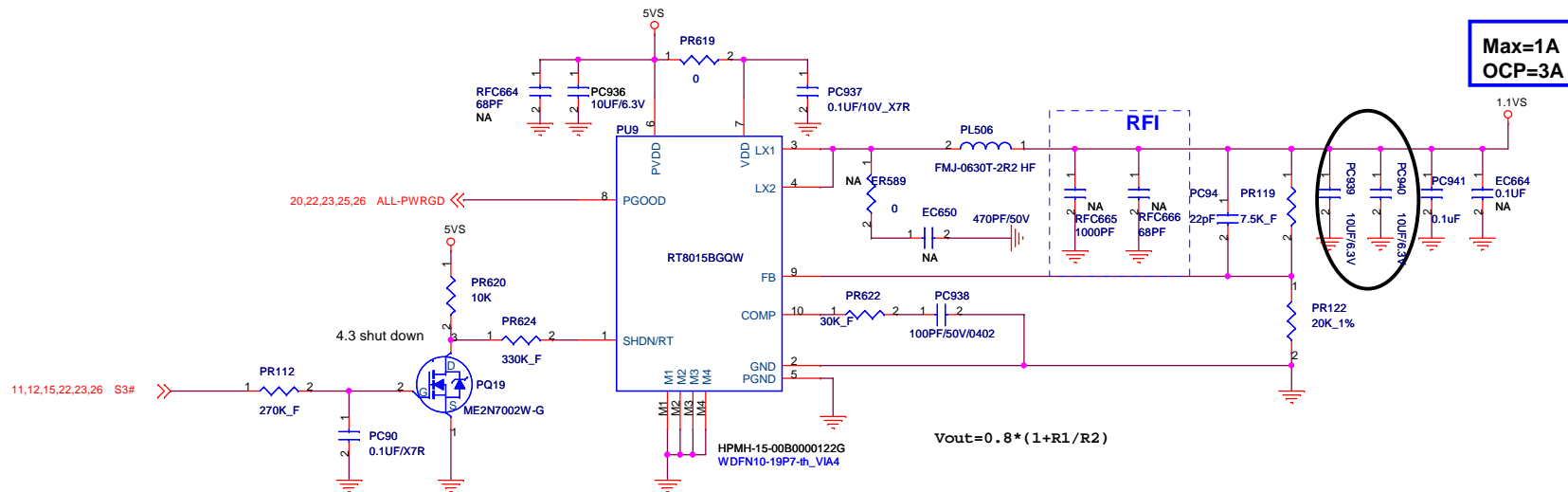
**FLEX** Computing

Project Name : H210UA1 Title : 1.5V / 0.75VS-VTT

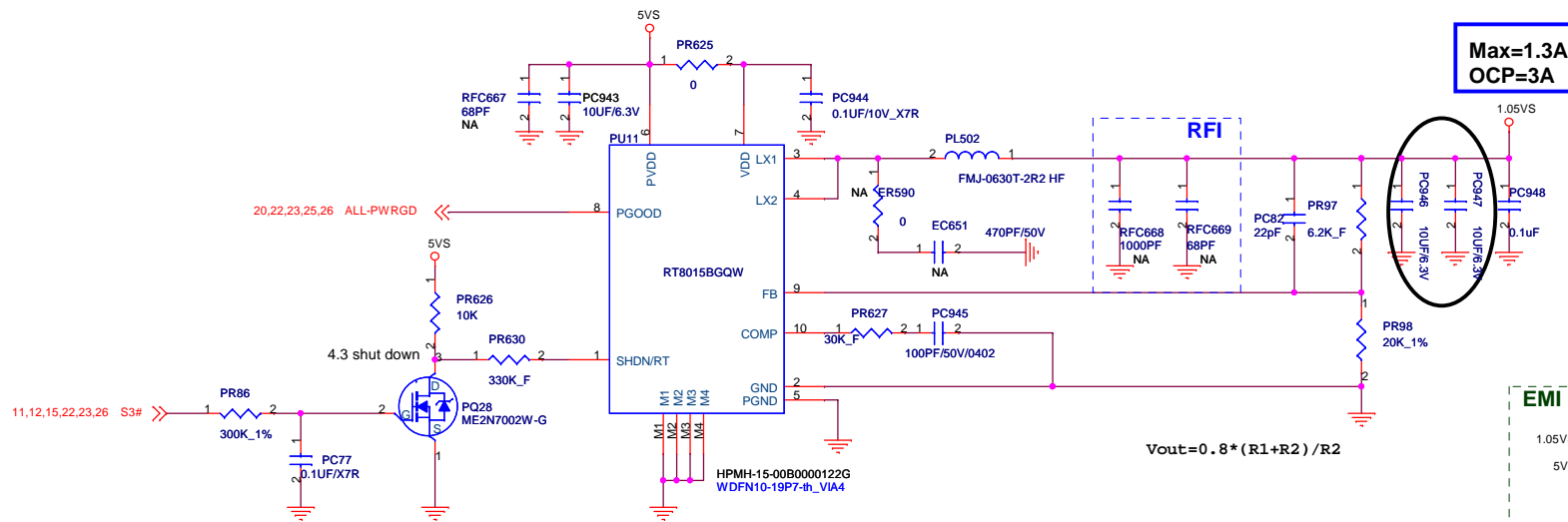
Size : Custom Document Number : HPMH-40GAB6000-D000 Rev : D

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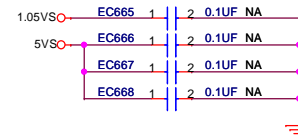
1.1VS



1.05VS



EMI



**FLEX** Computing

Project Name : H210UA1		Title : 1.1VS / 1.0VS	
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# 5V / 3.3VSTBY

Max=6.5A  
OCP=9.75A  
Freq=300KHz

Max=6A  
OCP=9A  
Freq=375KHz

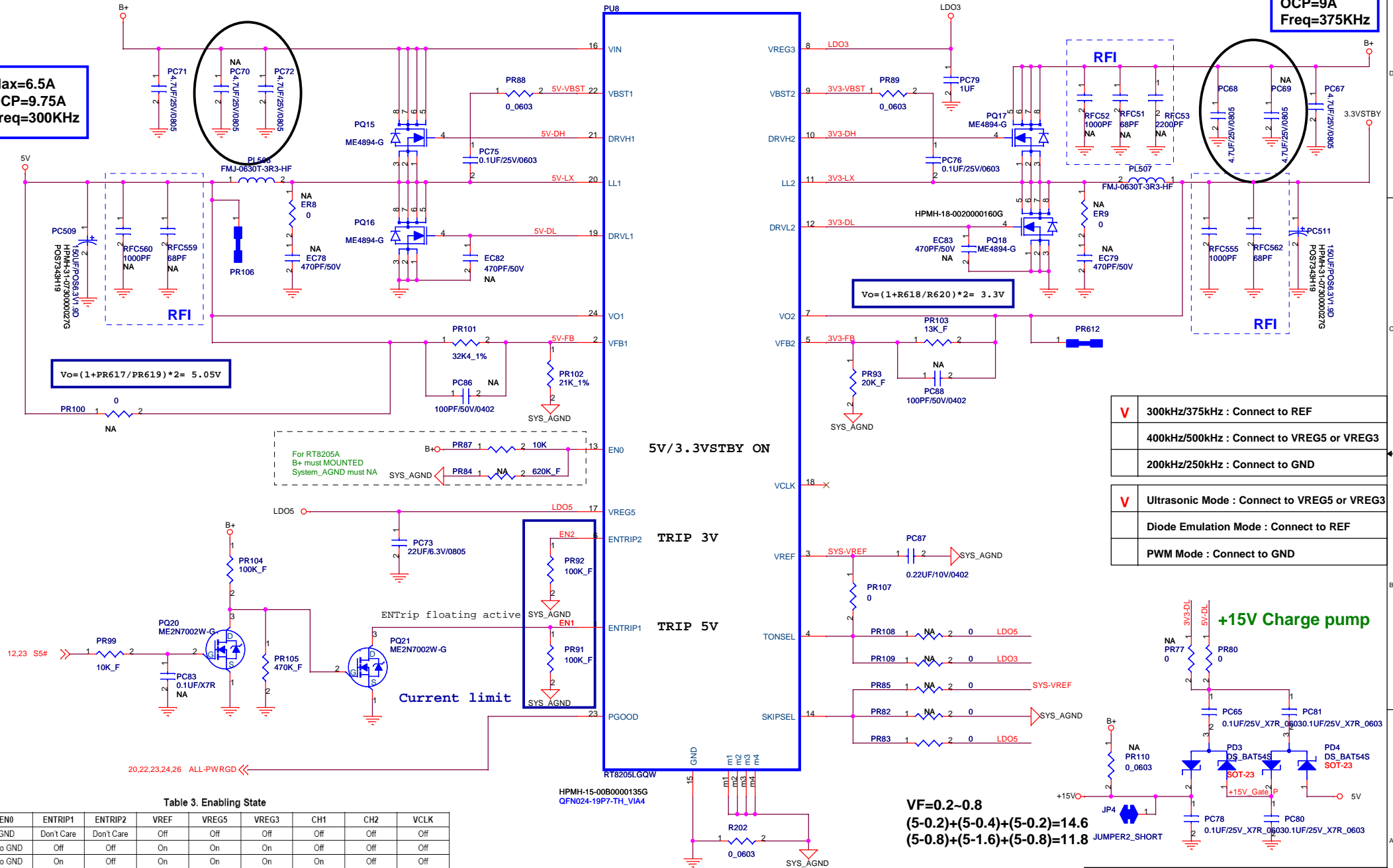


Table 3. Enabling State

EN0	ENTRIP1	ENTRIP2	VREF	VREG5	VREG3	CH1	CH2	VCLK
GND	Don't Care	Don't Care	Off	Off	Off	Off	Off	Off
R to GND	Off	Off	On	On	On	Off	Off	Off
R to GND	On	Off	On	On	On	On	Off	Off
R to GND	Off	On	On	On	On	On	On	Off
R to GND	On	On	On	On	On	On	On	Off
Open	Off	Off	On	On	On	Off	Off	Off
Open	On	Off	On	On	On	On	Off	On
Open	Off	On	On	On	On	Off	On	Off
Open	On	On	On	On	On	On	On	On

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S4/S3 OFF

5VS

Control HDMI CONN 5V

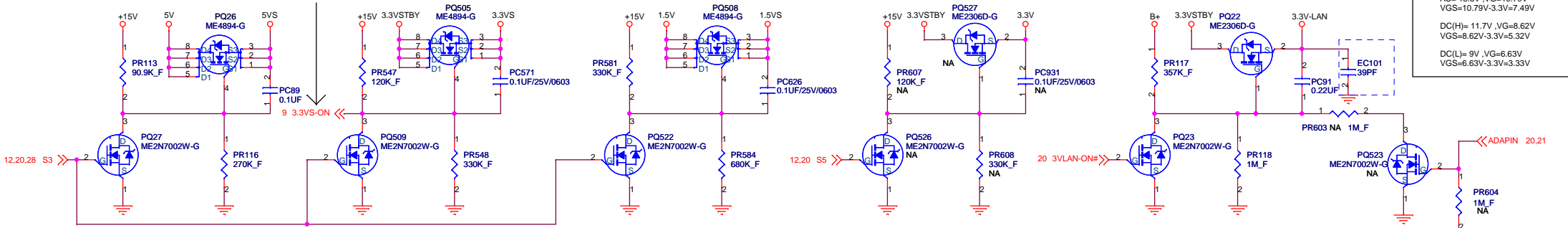
3.3VS

1.5VS

3.3V

3.3V-LAN

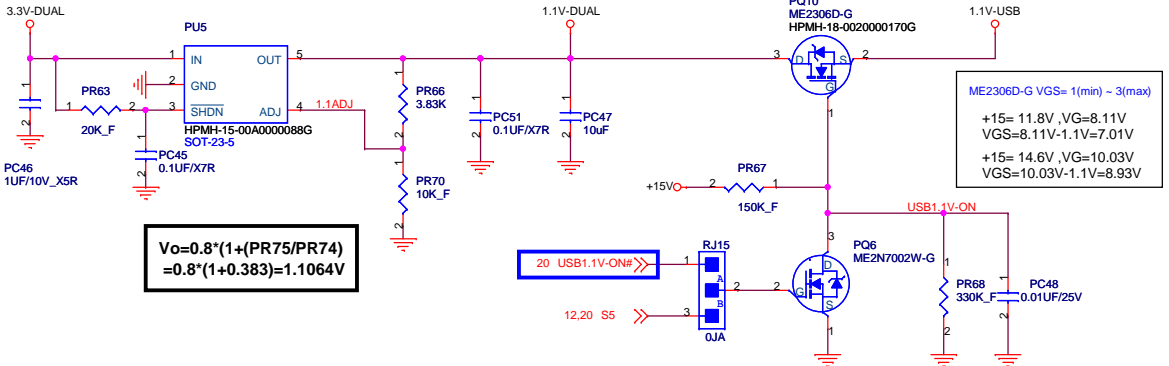
rise time 1 ~100ms  
AC= 18.5V ,VG=10.79V  
VGS=10.79V-3.3V=7.49V  
DC(H)= 11.7V ,VG=8.62V  
VGS=8.62V-3.3V=5.32V  
DC(L)= 9V ,VG=6.63V  
VGS=6.63V-3.3V=3.33V



1.1VSTBY / 1.1V\_USB

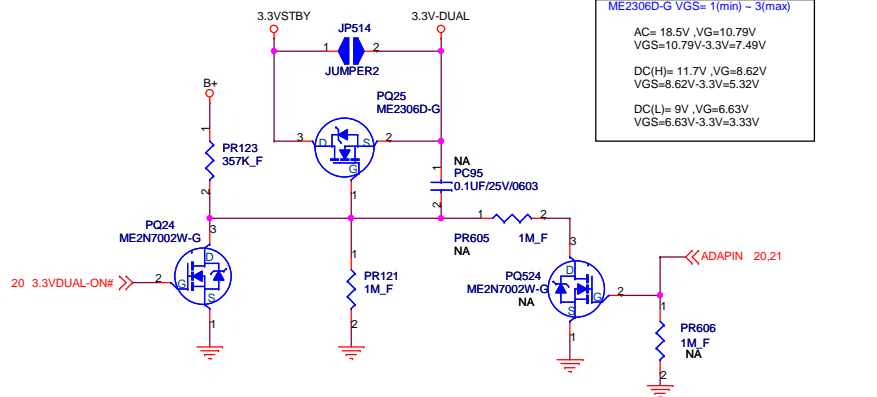
200mA

150mA



3.3V-DUAL

ME2306D-G VGS= 1(min) ~ 3(max)  
AC= 18.5V ,VG=10.79V  
VGS=10.79V-3.3V=7.49V  
DC(H)= 11.7V ,VG=8.62V  
VGS=8.62V-3.3V=5.32V  
DC(L)= 9V ,VG=6.63V  
VGS=6.63V-3.3V=3.33V



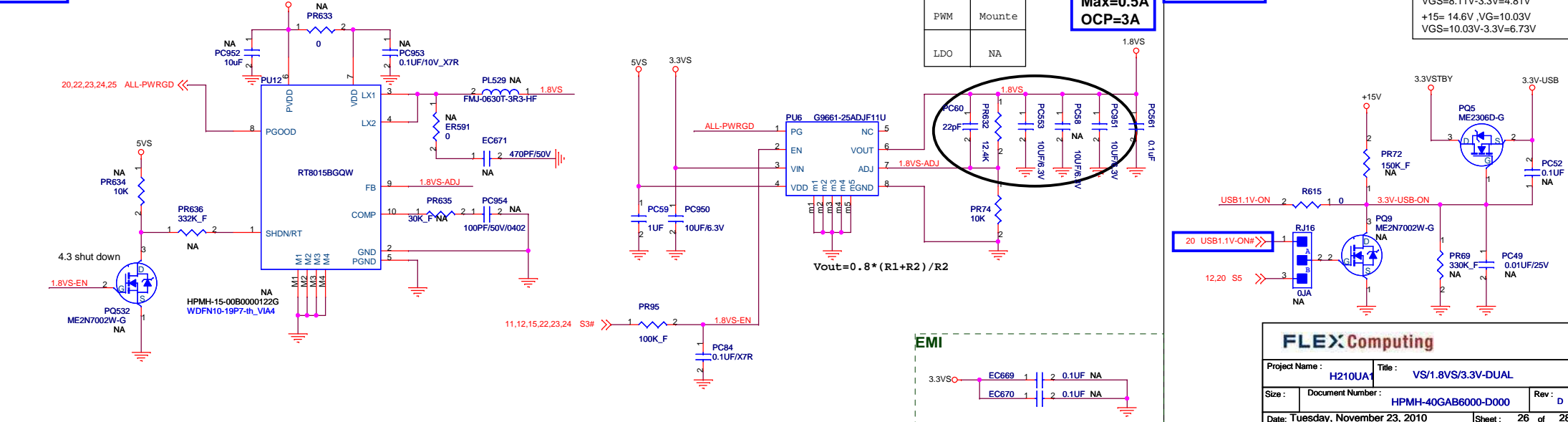
1.8VS

PC60 setting

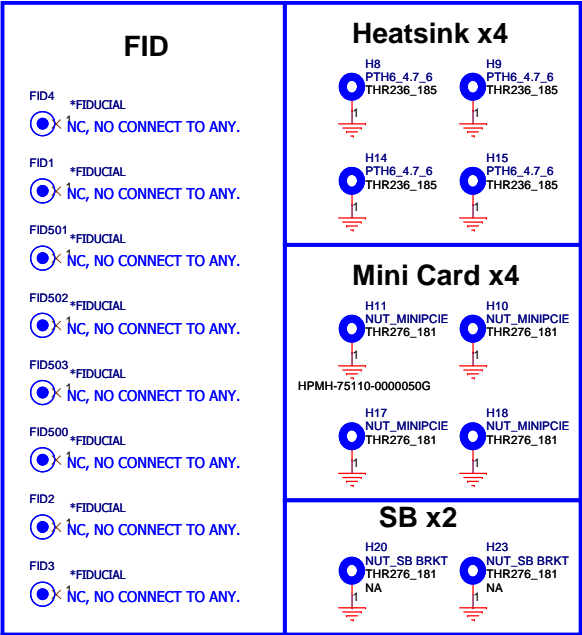
Max=0.5A  
OCP=3A

3.3V-USB

ME2306D-G VGS= 1(min) ~ 3(max)  
+15= 11.8V ,VG=8.11V  
VGS=8.11V-3.3V=4.81V  
+15= 14.6V ,VG=10.03V  
VGS=10.03V-3.3V=6.73V



FLEXComputing



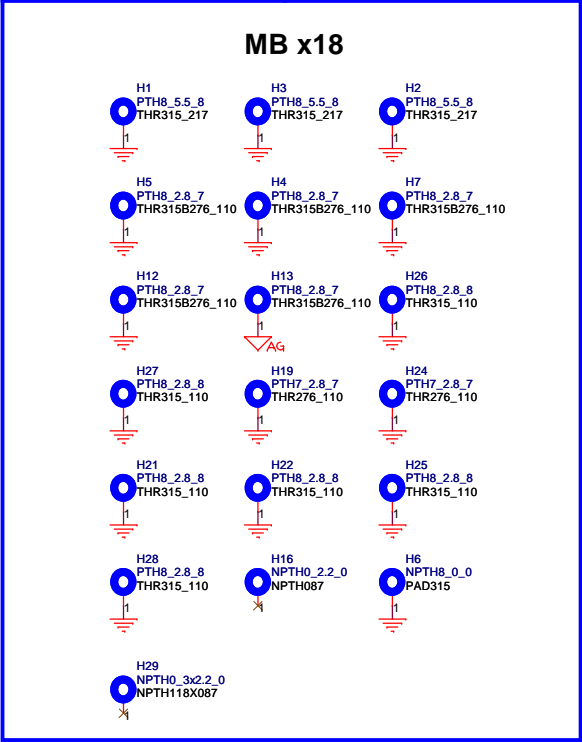
030 list

Type	Q'ty	P/N	Description
CPU Support	1	HPMH-B2985120G00001	BRKT CPU SUPPORT G AB600
SCREW	2	HPMH-7030730000069G	SCREW PH M1.6*0.35 L3 D3.6 T0.8 G AB600
RTC BATTERY CABLE	1	HPMH-B2985050G00001	CABLE ASSY RTC BATTERY G AB600
MYLAR	1	HPMH-B2985111G00001	MYLAR CPU MB TOP G AB600
MYLAR	1	HPMH-B2985111G00003	MYLAR FAN MB BOT G AB600
MYLAR	1	HPMH-B2985111G00004	MYLAR IO L MB BOT G AB600
MYLAR	1	HPMH-B2985111G00005	MYLAR IO R MB TOP G AB600
MYLAR	1	HPMH-B2985111G00006	MYLAR KB MB TOP G AB600
MYLAR	1	HPMH-B2985111G00008	MYLAR SB MB BOT G AB600
MYLAR	1	HPMH-B2985111G00010	MYLAR WWAN MB BOT G AB600
KAPTON	1	HPMH-B2985111G00035	KAPTON CPU AMD G AB600
KAPTON	1	HPMH-B2985111G00044	KAPTON FOR SB G AB600
SPONGE	2	HPMH-B2985111G00037	SPONGE L5.5W4H3 G AB600
SPONGE	4	HPMH-B2985111G00045	SPONGE L4.8*W4*H2.55 G AB600
GASKET	1	HPMH-B2985111G00048	GASKET W8*H1*L30 G AB600
GASKET	1	HPMH-B2985111G00049	GASKET W5*H0.7*L25 G AB600
GASKET	1	HPMH-B2985111G00050	GASKET W3*H1*L25 G AB600
CONDUCTIVE TAPE	1	HPMH-B2985111G00039	CONDUCTIVE TAPE FOR HDMI G AB600
CONDUCTIVE TAPE	1	HPMH-B2985111G00040	CONDUCTIVE TAPE W7*L12 G AB600
CONDUCTIVE FABRIC	2	HPMH-B2985111G00041	CONDUCTIVE FABRIC W7*T0.25*L10 G AB600
CONDUCTIVE TAPE	1	HPMH-B2985111G00042	CONDUCTIVE TAPE W6*L26 G AB600
RUBBER	2	HPMH-B2985111G00055	RUBBER FOR MB TOP G AB600

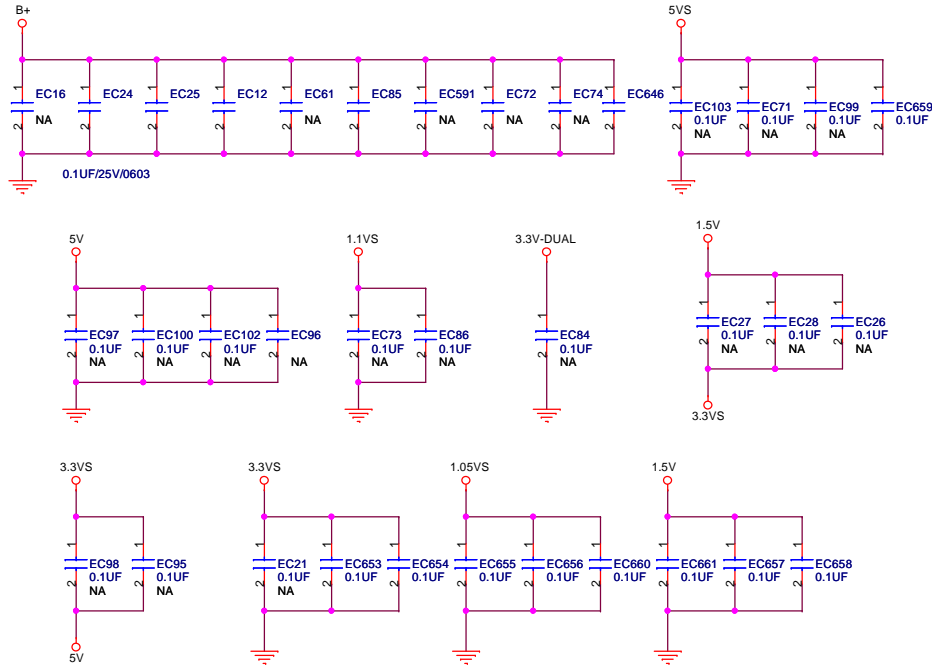
Normal short list

Location	Footprint
ER10 ER11 ER12 ER13 ER14 ER15 ER16 ER17 PR16 PR62 PR64 PR582 PR583 PR590 PR598 PR599 PR609 R91 R175 R205 R208 R209 R212 R218 R562 R571 R604 R605 R607 R612 R613 R614	PAD0402-SHORT5

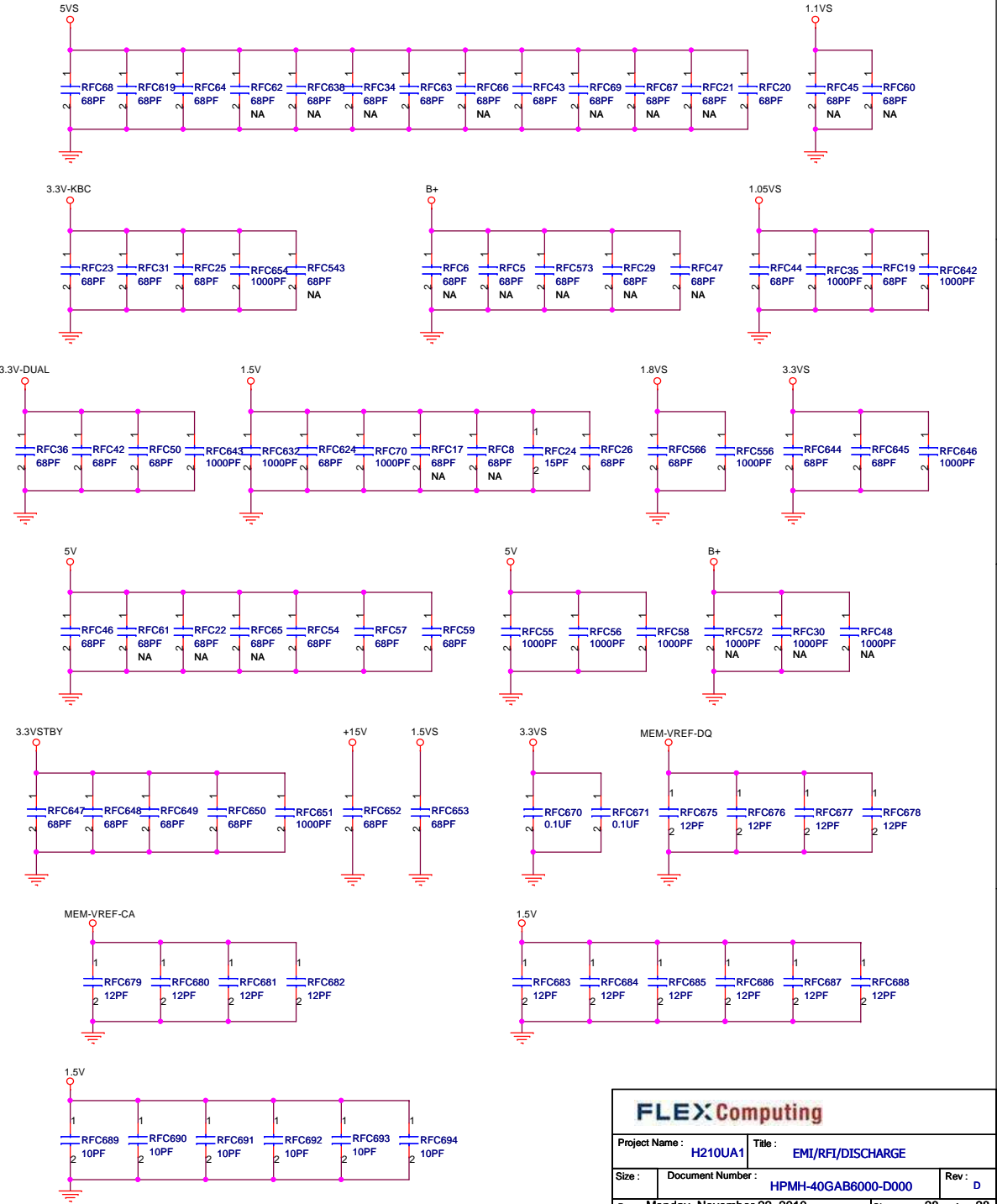
Location	Footprint
PR24 PR106 PR612	PAD0402-SHORT
ER3 ER4 ER6 ER7	PAD0603-SHORT
JP4	PAD-JUMPER-SHORT



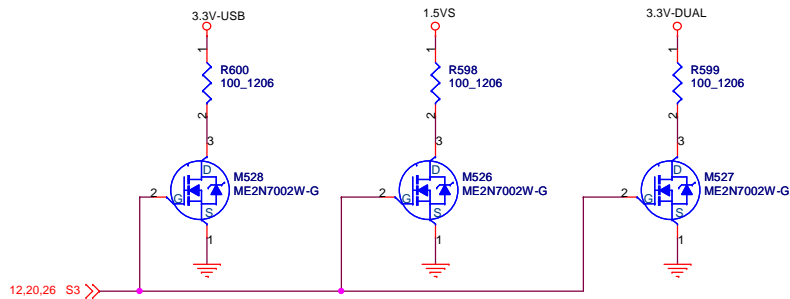
## EMI Caps



## RFI Caps



## Discharge



**FLEX** Computing

Project Name : H210UA1 Title : EMI/RFI/DISCHARGE

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